CS60010: Deep Learning

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LeNet-5 (LeCun, 1998)



The original Convolutional Neural Network model goes back to 1989 (LeCun)

Fully Connected Layer





Locally Connected Layer



Locally Connected Layer



Share the same parameters across different locations (assuming input is stationary): Convolutions with learned kernels









Grayscale Image

Convolve image with kernel having weights \mathbf{w} (learned by backpropagation)

Lecture 7 Convolutional Neural Networks

















What is the number of parameters?

Output Size

We used stride of 1, kernel with receptive field of size 3 by 3

Output size:

$$\frac{N-K}{S} + 1$$

In previous example: N = 6, K = 3, S = 1, Output size = 4 For N = 8, K = 3, S = 1, output size is 6

The replicated feature approach

- Use many different copies of the same feature detector with different positions.
 - reduces the number of free parameters to be learned.
- Use several different feature types, each with its own map of replicated detectors.
 - Allows each patch of image to be represented in several ways.





Learn Multiple Filters



Backpropagation with weight constraints

- It's easy to modify the backpropagation algorithm to incorporate linear constraints between the weights.
- We compute the gradients as usual, and then modify the gradients so that they satisfy the constraints.
 - So if the weights started off satisfying the constraints, they will continue to satisfy them.

To constrain: $w_1 = w_2$ *we need*: $\Delta w_1 = \Delta w_2$

compute:
$$\frac{\partial E}{\partial w_1}$$
 and $\frac{\partial E}{\partial w_2}$

use
$$\frac{\partial E}{\partial w_1} + \frac{\partial E}{\partial w_2}$$
 for w_1 and w_2

What does replicating the feature detectors achieve?

• Equivariant activities: Replicated features do not make the neural activities invariant to translation. The activities are equivariant.



 Invariant knowledge: If a feature is useful in some locations during training, detectors for that feature will be available in all locations during testing.



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32x32x3 image



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32x32x3 image



5x5x3 filter

Convolve the filter with the image i.e. "slide over the image spatially, computing dot products"

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For example, if we had 6 5x5 filters, we'll get 6 separate activation maps:



We stack these up to get a "new image" of size 28x28x6!

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ConvNet is a sequence of Convolution Layers, interspersed with activation functions

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ConvNet is a sequence of Convolutional Layers, interspersed with activation functions



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Feature visualization of convolutional net trained on ImageNet from [Zeiler & Fergus 2013]

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Feature visualization of convolutional net trained on ImageNet from [Zeiler & Fergus 2013]



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[From recent Yann LeCun slides]



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7x7 input (spatially) assume 3x3 filter

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7x7 input (spatially) assume 3x3 filter

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7x7 input (spatially) assume 3x3 filter

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7x7 input (spatially) assume 3x3 filter

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7x7 input (spatially) assume 3x3 filter

=> 5x5 output

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7x7 input (spatially) assume 3x3 filter applied **with stride 2**

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7x7 input (spatially) assume 3x3 filter applied **with stride 2**

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7x7 input (spatially) assume 3x3 filter applied with stride 2 => 3x3 output!

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7x7 input (spatially) assume 3x3 filter applied **with stride 3?**

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7x7 input (spatially) assume 3x3 filter applied **with stride 3?**

doesn't fit!

cannot apply 3x3 filter on 7x7 input with stride 3.

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Output size: (N - F) / stride + 1

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e.g. N = 7, F = 3: stride 1 => (7 - 3)/1 + 1 = 5stride 2 => (7 - 3)/2 + 1 = 3stride 3 => (7 - 3)/3 + 1 = 2.33 :\



In practice: Common to zero pad the border

e.g. input 7x7
3x3 filter, applied with stride 1
pad with 1 pixel border => what is the
output?

(recall:) (N - F) / stride + 1

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In practice: Common to zero pad the border



e.g. input 7x7 **3x3** filter, applied with stride 1
pad with 1 pixel border => what is the output?

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7x7 output!

In practice: Common to zero pad the border



e.g. input 7x7
3x3 filter, applied with stride 1
pad with 1 pixel border => what is the output?

7x7 output!

in general, common to see CONV layers with stride 1, filters of size FxF, and zero-padding with (F-1)/2. (will preserve size spatially) e.g. $F = 3 \Longrightarrow$ zero pad with 1 $F = 5 \Longrightarrow$ zero pad with 2 $F = 7 \Longrightarrow$ zero pad with 3

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Remember back to...

E.g. 32x32 input convolved repeatedly with 5x5 filters shrinks volumes spatially! (32 -> 28 -> 24 ...). Shrinking too fast is not good, doesn't work well.



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Output volume size: ?

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Output volume size: (32+2*2-5)/1+1 = 32 spatially, so 32x32x10

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Number of parameters in this layer?

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Number of parameters in this layer? each filter has 5*5*3 + 1 = 76 params (+1 for bias) => 76*10 = 760

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Summary. To summarize, the Conv Layer:

- Accepts a volume of size $W_1 imes H_1 imes D_1$
- Requires four hyperparameters:
 - Number of filters K,
 - \circ their spatial extent F,
 - \circ the stride S,
 - $\circ\;$ the amount of zero padding P.
- Produces a volume of size $W_2 imes H_2 imes D_2$ where:
 - $W_2 = (W_1 F + 2P)/S + 1$
 - $\circ~H_2 = (H_1 F + 2P)/S + 1$ (i.e. width and height are computed equally by symmetry)
 - $\circ D_2 = K$
- With parameter sharing, it introduces F · F · D₁ weights per filter, for a total of (F · F · D₁) · K weights and K biases.
- In the output volume, the d-th depth slice (of size $W_2 \times H_2$) is the result of performing a valid convolution of the d-th filter over the input volume with a stride of S, and then offset by d-th bias.

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Common settings:

Summary. To summarize, the Conv Layer:

- Accepts a volume of size $W_1 imes H_1 imes D_1$
- Requires four hyperparameters:
 - Number of filters K,
 - $\circ\;$ their spatial extent F ,
 - \circ the stride S,
 - $\circ\;$ the amount of zero padding P.
- Produces a volume of size $W_2 imes H_2 imes D_2$ where:

•
$$W_2 = (W_1 - F + 2P)/S + 1$$

K = (powers of 2, e.g. 32, 64, 128, 512)

- F = 3, S = 1, P = 1
- F = 5, S = 1, P = 2
- F = 5, S = 2, P = ? (whatever fits)

-
$$F = 1, S = 1, P = 0$$

- $H_2 = (H_1 F + 2P)/S + 1$ (i.e. width and height are computed equally by symmetry) • $D_2 = K$
- With parameter sharing, it introduces $F \cdot F \cdot D_1$ weights per filter, for a total of $(F \cdot F \cdot D_1) \cdot K$ weights and K biases.
- In the output volume, the d-th depth slice (of size $W_2 \times H_2$) is the result of performing a valid convolution of the d-th filter over the input volume with a stride of S, and then offset by d-th bias.

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Convnets



 Convolutional Layer, Pooling Layer, and Fully-Connected Layer every layer of a ConvNet transforms one volume of activations to another through a differentiable function.

Pooling Layer



Q.: how can we make the detection robust to the exact location of the eye?



Pooling Layer

By "pooling" (e.g., taking max) filter responses at different locations we gain robustness to the exact spatial location of features.



Pooling layer

- makes the representations smaller and more manageable
- operates over each activation map independently:



MAX POOLING

Single depth slice



y

max pool with 2x2 filters and stride 2

6	8
3	4

MAX POOLING

Single depth slice



y

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6

3

8

4

General pooling

• Other pooling functions: Average pooling, L2-norm pooling



- Backpropagation. the backward pass for a max(x, y) operation routes the gradient to the input that had the highest value in the forward pass.
- Hence, during the forward pass of a pooling layer you may keep track of the index of the max activation (sometimes also called the switches) so that gradient routing is efficient during backpropagation.

Getting rid of pooling

- 1. Striving for Simplicity: The All Convolutional Net proposes to discard the pooling layer and have an architecture that only consists of repeated CONV layers.
- To reduce the size of the representation they suggest using larger stride in CONV layer once in a while.
- Argument:
 - The purpose of pooling layers is to perform dimensionality reduction to widen subsequent convolutional layers' receptive fields.
 - The same effect can be achieved by using a convolutional layer: using a stride of 2 also reduces the dimensionality of the output and widens the receptive field of higher layers.
- The resulting operation differs from a max-pooling layer in that
 - it cannot perform a true max operation
 - it allows pooling across input channels.

- 2. The second is Very Deep Convolutional Networks for Large-Scale Image Recognition.
- The core idea here is that hand-tuning layer kernel sizes to achieve optimal receptive fields (say, 5×5 or 7×7) can be replaced by simply stacking homogenous 3×3 layers.
- The same effect of widening the receptive field is then achieved by layer composition rather than increasing the kernel size
 - three stacked 3×3 have a 7×7 receptive field.
 - At the same time, the number of parameters is reduced:
 - a 7×7 layer has 81% more parameters than three stacked 3×3 layers.

[ConvNetJS demo: training on CIFAR-10]

http://cs.stanford.edu/people/karpathy/convnetjs/demo/cifar10.html

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Case Studies

- **LeNet**. The first successful applications of Convolutional Networks were developed by Yann LeCun in 1990's. was used to read zip codes, digits, etc.
- AlexNet. popularized Convolutional Networks in Computer Vision, developed by Alex Krizhevsky, Ilya Sutskever and Geoff Hinton.
- The AlexNet was submitted to the <u>ImageNet ILSVRC challenge</u> in 2012 and significantly outperformed the second runner-up (top 5 error of 16% compared to runner-up with 26% error). The Network had a very similar architecture to LeNet, but was deeper, bigger, and featured Convolutional Layers stacked on top of each other
- **ZF Net**. The ILSVRC 2013 winner was a Convolutional Network from Matthew Zeiler and Rob Fergus. It was an improvement on AlexNet by tweaking the architecture hyperparameters, in particular by expanding the size of the middle convolutional layers and making the stride and filter size on the first layer smaller.

LeNet

- Yann LeCun and his collaborators developed a really good recognizer for handwritten digits by using backpropagation in a feedforward net with:
 - Many hidden layers
 - Many maps of replicated units in each layer.
 - Pooling of the outputs of nearby replicated units.
 - A wide net that can cope with several characters at once even if they overlap.
 - A clever way of training a complete system, not just a recognizer.
- This net was used for reading ~10% of the checks in North America.
- demos of LENET at http://yann.lecun.com

Case Study: LeNet-5

[LeCun et al., 1998]



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Conv filters were 5x5, applied at stride 1

Subsampling (Pooling) layers were 2x2 applied at stride 2

i.e. architecture is [CONV-POOL-CONV-POOL-CONV-FC]

875707734 9->4 8->0 7->8 5->3 8->7 0->6 3->7 2->7 8->3 9->4 838409914 8->2 5->3 4->8 3->9 6->0 9->8 4->9 6->1 9->4 9->1 9013250000 9->4 2->0 6->1 3->5 3->2 9->5 6->0 6->0 6->0 6->8 4794794999 4->6 7->3 9->4 4->6 2->7 9->7 4->3 9->4 9->4 9->4 7483868839 8->7 4->2 8->4 3->5 8->4 6->5 8->5 3->8 3->8 9->8 1960619141 1->5 9->8 6->3 0->2 6->5 9->5 0->7 1->6 4->9 2->1 1 8 4 7 7 6 9 6 5 5 2->8 8->5 4->9 7->2 7->2 6->5 9->7 6->1 5->6 5->0 4->9 2->8

The 82 errors made by LeNet5

Notice that most of the errors are cases that people find quite easy.

The human error rate is probably 20 to 30 errors but nobody has had the patience to measure it.

The ILSVRC-2012 competition on ImageNet

- The dataset has 1.2 million high-resolution training images.
- The classification task:
 - Get the "correct" class in your top 5 bets. There are 1000 classes.
- The localization task:
 - For each bet, put a box around the object. Your box must have at least 50% overlap with the correct box.

- Some of the best existing computer vision methods were tried on this dataset by leading computer vision groups from Oxford, INRIA, XRCE, ...
 - Computer vision systems use complicated multistage systems.
 - The early stages are typically hand-tuned by optimizing a few parameters.

Examples from the test set (with the network's guesses)

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Error rates on the ILSVRC-2012 competition

University of Tokyo

- Oxford University Computer Vision Group
- INRIA (French national research institute in CS) + XRCE (Xerox Research Center Europe)
- University of Amsterdam

classification

16.4%

classification & localization

34.1%

- 26.1% 53.6%
- 26.9% 50.0%
- 27.0%
- 29.5%

A neural network for ImageNet

- Alex Krizhevsky (NIPS 2012) developed a very deep convolutional neural net of the type pioneered by Yann Le Cun. Its architecture was:
 - 7 hidden layers not counting some max pooling layers.
 - The early layers were convolutional.
 - The last two layers were globally connected.

- The activation functions were:
 - Rectified linear units in every hidden layer. These train much faster and are more expressive than logistic units.
 - Competitive normalization to suppress hidden activities when nearby units have stronger activities. This helps with variations in intensity.

Tricks that significantly improve generalization

- Train on random 224x224 patches from the 256x256 images to get more data. Also use left-right reflections of the images.
 - At test time, combine the opinions from ten different patches: The four 224x224 corner patches plus the central 224x224 patch plus the reflections of those five patches.
- Use "dropout" to regularize the weights in the globally connected layers (which contain most of the parameters).
 - Dropout means that half of the hidden units in a layer are randomly removed for each training example.
 - This stops hidden units from relying too much on other hidden units.
The hardware required for Alex's net

- He uses a very efficient implementation of convolutional nets on two Nvidia GTX 580 Graphics Processor Units (over 1000 fast little cores)
 - GPUs are very good for matrix-matrix multiplies.
 - GPUs have very high bandwidth to memory.
 - This allows him to train the network in a week.
 - It also makes it quick to combine results from 10 patches at test time.
- We can spread a network over many cores if we can communicate the states fast enough.
- As cores get cheaper and datasets get bigger, big neural nets will improve faster than old-fashioned (*i.e.* pre Oct 2012) computer vision systems.

[Krizhevsky et al. 2012]





Input: 227x227x3 images

First layer (CONV1): 96 11x11 filters applied at stride 4

Output volume **[55x55x96]** Parameters: (11*11*3)*96 = **35K**

(227-11)/4+1 = 55

[Krizhevsky et al. 2012]



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Input: 227x227x3 images After CONV1: 55x55x96

Second layer (POOL1): 3x3 filters applied at stride 2

Q: what is the output volume size? Hint: (55-3)/2+1 = 27

[Krizhevsky et al. 2012]



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Input: 227x227x3 images After CONV1: 55x55x96

Second layer (POOL1): 3x3 filters applied at stride 2 Output volume: 27x27x96

Q: what is the number of parameters in this layer?

[Krizhevsky et al. 2012]



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Input: 227x227x3 images After CONV1: 55x55x96

Second layer (POOL1): 3x3 filters applied at stride 2 Output volume: 27x27x96 Parameters: 0!

[Krizhevsky et al. 2012]



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Input: 227x227x3 images After CONV1: 55x55x96 After POOL1: 27x27x96

• • •

[Krizhevsky et al. 2012]



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Full (simplified) AlexNet architecture: [227x227x3] INPUT [55x55x96] CONV1: 96 11x11 filters at stride 4, pad 0 [27x27x96] MAX POOL1: 3x3 filters at stride 2 [27x27x96] NORM1: Normalization layer [27x27x26] CONV2: 256 5x5 filters at stride 1, pad 2 [13x13x256] MAX POOL2: 3x3 filters at stride 2 [13x13x256] NORM2: Normalization layer [13x13x384] CONV3: 384 3x3 filters at stride 1, pad 1 [13x13x384] CONV4: 384 3x3 filters at stride 1, pad 1 [13x13x256] MAX POOL3: 3x3 filters at stride 1, pad 1 [13x13x256] CONV5: 256 3x3 filters at stride 1, pad 1 [6x6x256] MAX POOL3: 3x3 filters at stride 2 [4096] FC6: 4096 neurons [4096] FC7: 4096 neurons [4096] FC7: 4096 neurons

[Krizhevsky et al. 2012]

Full (simplified) AlexNet architecture:



[227x227x3] INPUT [55x55x96] CONV1: 96 11x11 filters at stride 4, pad 0 [27x27x96] MAX POOL1: 3x3 filters at stride 2 [27x27x96] NORM1: Normalization layer [27x27x26] CONV2: 256 5x5 filters at stride 1, pad 2 [13x13x256] MAX POOL2: 3x3 filters at stride 2 [13x13x256] NORM2: Normalization layer [13x13x384] CONV3: 384 3x3 filters at stride 1, pad 1 [13x13x384] CONV4: 384 3x3 filters at stride 1, pad 1 [13x13x256] MAX POOL2: 256 3x3 filters at stride 1, pad 1 [13x13x256] CONV5: 256 3x3 filters at stride 1, pad 1 [13x13x256] CONV5: 256 3x3 filters at stride 1, pad 1 [4096] FC6: 4096 neurons [4096] FC6: 4096 neurons [4096] FC7: 4096 neurons [4096] FC7: 4096 neurons

Details/Retrospectives:

- first use of ReLU
- used Norm layers (not common anymore)
- heavy data augmentation
- dropout 0.5
- batch size 128
- SGD Momentum 0.9
- Learning rate 1e-2, reduced by 10 manually when val accuracy plateaus
- L2 weight decay 5e-4
- 7 CNN ensemble: 18.2% -> 15.4%

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Case Studies

- GoogLeNet. The ILSVRC 2014 winner was a Convolutional Network from <u>Szegedy et al.</u> from Google.
- Its main contribution was the development of an *Inception Module* that dramatically reduced the number of parameters in the network (4M, compared to AlexNet with 60M).
- Uses Average Pooling instead of Fully Connected layers at the top of the ConvNet
- There are also several followup versions to the GoogLeNet, most recently <u>Inception-v4</u>.
- VGGNet. The runner-up in ILSVRC 2014 was the network from Karen Simonyan and Andrew Zisserman.
- Showed that the depth of the network is a critical component for good performance. Their final best network contains 16 CONV/FC layers
- and, apfeatures an extremely homogeneous architecture that only performs 3x3 convolutions and 2x2 pooling from the beginning to the end. Their <u>pretrained model</u> is available for plug and play use in Caffe. A downside of the VGGNet is that it is more expensive to evaluate and uses a lot more memory and parameters (140M). Most of these parameters are in the first fully connected layer, and it was since found that these FC layers can be removed with no performance downgrade, significantly reducing the number of necessary parameters.

"You need a lot of a data if you want to train/use CNNs"

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Transfer Learning

"You need a lot of a data if you want to train/use CNN."



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The Unreasonable Effectiveness of Deep Features





Low-level: Pool Classes separate in the deep representations and transfer to many tasks. [DeCAF] [Zeiler-Fergus]

Can be used as a generic feature

("CNN code" = 4096-D vector before classifier)



query image

nearest neighbors in the "code" space

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image	1. Train on
conv-64	Imagenet
conv-64	
maxpool	
conv-128	
conv-128	
maxpool	
conv-256	
conv-256	
maxpool	
maxpoor	
conv-512	
conv-512	
maxpool	
conv-512	
conv-512	
maxpool	
FC-4096	
FC-4096	
FC-1000	
softmax	

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image 1. Train on Imagenet conv-64 conv-64 maxpool conv-128 conv-128 maxpool conv-256 conv-256 maxpool conv-512 conv-512 maxpool conv-512 conv-512 maxpool FC-4096 FC-4096 FC-1000 softmax

2. If small dataset: fix all weights (treat CNN as fixed feature extractor), retrain only the classifier

i.e. swap the Softmax layer at the end

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image 1. Train on Imagenet conv-64 conv-64 maxpool conv-128 conv-128 maxpool conv-256 conv-256 maxpool conv-512 conv-512 maxpool conv-512 conv-512 maxpool FC-4096 FC-4096 FC-1000 softmax

2. If small dataset: fix all weights (treat CNN as fixed feature extractor), retrain only the classifier i.e. swap the Softmax layer at the end

image conv-64 conv-64 maxpool conv-128 conv-128 maxpool conv-256 conv-256 maxpool conv-512 conv-512 maxpool conv-512 conv-512 maxpool FC-4096 FC-4096 FC-1000 softmax

3. If you have medium sized dataset, "finetune" instead: use the old weights as initialization, train the full network or only some of the higher layers

retrain bigger portion of the network, or even all of it.

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		00020000	ConvNet C	onfiguration	1000	
$O_{22} = O_{12} + O_{22} + O$	A	A-LRN	В	С	D	E
Case Study: VGGINET	11 weight	11 weight	13 weight	16 weight	16 weight	19 weight
	layers	layers	layers	layers	layers	layers
Simonyon and Ziacormon 2014		i	nput (224×2	24 RGB imag)	
[Simonyan and Zisserman, 2014]	conv3-64	conv3-64	conv3-64	conv3-64	conv3-64	conv3-64
		LRN	conv3-64	conv3-64	conv3-64	conv3-64
			max	pool		
Only 3x3 CONV stride 1, pad 1	conv3-128	conv3-128	conv3-128	conv3-128	conv3-128	conv3-128
and 2x2 MAX POOL stride 2			conv3-128	conv3-128	conv3-128	conv3-128
and 2x2 MAX POOL Since 2	103317	0.000	max	pool		
	conv3-256	conv3-256	conv3-256	conv3-256	conv3-256	conv3-256
	conv3-256	conv3-256	conv3-256	conv3-25	conv3-256	conv3-256
				conv1-256	conv3-256	conv3-256
					2	conv3-256
	2.512	0.510	max	pool		0.510
	conv3-512	conv3-512	conv3-512	conv3-512	conv3-512	conv3-512
	conv3-512	conv3-512	conv3-512	conv3-512	conv3-512	conv3-512
				conv1-512	conv3-512	conv3-512
						conv3-512
	aam.2 512	aam.2 512	max	p001	20002 512	aam.2 512
hest model	conv3-512	conv3-512	conv3-512	conv3-512	conv3-512	conv3-512
	conv3-312	01173-312	COIIV 5-512	conv1-512	conv3-512	conv3-512
				CONVI-512	CONV5-512	conv3-512
			may	nool		011/5 512
			FC-	4096		
			FC-	4096		
11.2% top 5 error in ILSVRC 2013			FC-	1000		
			soft	-max		
->			5011			
7.3% top 5 error		T11 0 1				
		lable 2:	umber of pa	arameters (1	n millions).	

rable 2. Humber of parameters (in minibils).								
Network	A,A-LRN	B	С	D	E			
Number of parameters	133	133	134	138	144			

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INPUT: [224x224x3] memory: 224*224*3=150K params: 0 (not counting biases)	
CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*3)*64 = 1,728	Conv
CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*64)*64 = 36,864	B
POOL2: [112x112x64] memory: 112*112*64=800K params: 0	13 wei
CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*64)*128 = 73,728	laver
CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*128)*128 = 147,456	(00)
POOL2: [56x56x128] memory: 56*56*128=400K params: 0	put (224
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*128)*256 = 294,912	conv3-
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824	conv3
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824	
POOL2: [28x28x256] memory: 28*28*256=200K params: 0	conv3-
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*256)*512 = 1,179,648	conv3-
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296	
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296	conv3-
POOL2: [14x14x512] memory: 14*14*512=100K params: 0	conv3-
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296	
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296	
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296	
POOL2: [7x7x512] memory: 7*7*512=25K params: 0	conv3-
FC: [1x1x4096] memory: 4096 params: 7*7*512*4096 = 102,760,448	conv3-
FC: [1x1x4096] memory: 4096 params: 4096*4096 = 16,777,216	1201201004
FC: [1x1x1000] memory: 1000 params: 4096*1000 = 4,096,000	

B 13 weight layers put (224 × 224 conv3-64 conv3-64 conv3-128 conv3-128 conv3-256 conv3-256	C 16 weight layers RGB image conv3-64 conv3-64 conv3-64 conv3-128 conv3-128 conv3-128 conv3-256	D 16 weight layers conv3-64 conv3-64 conv3-128 conv3-128	
13 weight layers put (224 × 224 conv3-64 conv3-64 conv3-128 conv3-128 conv3-128 conv3-256 conv3-256	16 weight layers RGB image conv3-64 conv3-64 pol conv3-128 pol conv3-128 pol conv3-256	16 weight layers conv3-64 conv3-64 conv3-128 conv3-128	
layers put (224 × 224 conv3-64 conv3-64 conv3-128 conv3-128 conv3-256 conv3-256	layers RGB image conv3-64 conv3-64 conv3-128 conv3-128 conv3-128 conv3-256	layers conv3-64 conv3-64 conv3-128 conv3-128 conv3-256	
put (224 × 224 conv3-64 conv3-64 conv3-128 conv3-128 conv3-128 conv3-256 conv3-256 conv3-256	RGB image conv3-64 conv3-64 pol conv3-128 conv3-128 pol conv3-256 conv3-256	conv3-64 conv3-64 conv3-128 conv3-128	
conv3-64 conv3-64 conv3-128 conv3-128 conv3-128 conv3-256 conv3-256	conv3-64 conv3-64 pol conv3-128 conv3-128 pol conv3-256 conv3-256	conv3-64 conv3-64 conv3-128 conv3-128	
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conv3-128 conv3-128 conv3-128 conv3-128 conv3-256 conv3-	conv3-128 conv3-128 pol conv3-256 conv3-256	conv3-128 conv3-128	COI COI
conv3-128 o maxpo conv3-256 o conv3-256 o o	conv3-128 ool conv3-256 conv3-256	conv3-128	co
maxpo conv3-256 conv3-256	conv3-256	conv3-256	
conv3-256 conv3-256	conv3-256	conv3-256	
conv3-256	conv3-256		CO
	00000 200	conv3-256	CO
	conv1-256	conv3-256	CO
			col
maxpo	ool		
conv3-512 (conv3-512	conv3-512	CO
conv3-512	conv3-512	conv3-512	CO
(conv1-512	conv3-512	CO
			CO
maxpo	ool		
conv3-512	conv3-512	conv3-512	CO
conv3-512	conv3-512	conv3-512	CO
(conv1-512	conv3-512	CO
	1112		CO
maxpo	ool		
FC-40	96		
FC-40	96		
FC-10	00		
soft-m	ax		

INPUT: [224x224x3] memory: 224*224*3=150K params: 0 (not counting biases)	
CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*3)*64 = 1,728	ConvNe
CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*64)*64 = 36,864	В
POOL2: [112x112x64] memory: 112*112*64=800K params: 0	13 weig
CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*64)*128 = 73,728	lavers
CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*128)*128 = 147,456	put (224
POOL2: [56x56x128] memory: 56*56*128=400K params: 0	put (224
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*128)*256 = 294,912	conv3-6
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824	conv3-6
CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824	1
POOL2: [28x28x256] memory: 28*28*256=200K params: 0	conv3-12
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*256)*512 = 1,179,648	conv3-12
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296	1
CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296	conv3-25
POOL2: [14x14x512] memory: 14*14*512=100K params: 0	conv3-25
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296	
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296	
CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296	1
POOL2: [7x7x512] memory: 7*7*512=25K params: 0	conv3-51
FC: [1x1x4096] memory: 4096 params: 7*7*512*4096 = 102,760,448	conv3-5
FC: [1x1x4096] memory: 4096 params: 4096*4096 = 16,777,216	
FC: [1x1x1000] memory: 1000 params: 4096*1000 = 4,096,000	
	1
	2.51

TOTAL memory: 24M * 4 bytes ~= 93MB / image (only forward! ~*2 for bwd) TOTAL params: 138M parameters

	ConvNet Configuration							
	В	С	D					
	13 weight	16 weight	16 weight	19				
	layers	layers	layers					
)	put (224×22	24 RGB image						
	conv3-64	conv3-64	conv3-64	CC				
	conv3-64	conv3-64	conv3-64	cc				
	max	pool						
	conv3-128	conv3-128	conv3-128	CO				
	conv3-128	conv3-128	conv3-128	co				
	max	pool	0.000					
	conv3-256	conv3-256	conv3-256	CO				
	conv3-256	conv3-256	conv3-256	co				
		conv1-256	conv3-256	co				
				CO				
	max	pool						
	conv3-512	conv3-512	conv3-512	CO				
	conv3-512	conv3-512	conv3-512	co				
		conv1-512	conv3-512	co				
	0			CO				
	max	pool						
	conv3-512	conv3-512	conv3-512	CO				
	conv3-512	conv3-512	conv3-512	CO				
		conv1-512	conv3-512	co				
		to the second		CO				
	max	pool						
	FC-	4096						
	FC-	4096						
	FC-	1000						
	soft-	max						

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INPUT: [224x224x3] memory: 224*224*3=150K params: 0 CONV3-64: [224x224x64] memory: 224*224*64=3.2M params: (3*3*3)*64 = 1,728 CONV3-64: [224x224x64] memory: **224*224*64=3.2M** params: (3*3*64)*64 = 36,864 Note: POOL2: [112x112x64] memory: 112*112*64=800K params: 0 CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*64)*128 = 73,728 Most memory is in CONV3-128: [112x112x128] memory: 112*112*128=1.6M params: (3*3*128)*128 = 147,456 early CONV POOL2: [56x56x128] memory: 56*56*128=400K params: 0 CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*128)*256 = 294,912 CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824 CONV3-256: [56x56x256] memory: 56*56*256=800K params: (3*3*256)*256 = 589,824 POOL2: [28x28x256] memory: 28*28*256=200K params: 0 CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*256)*512 = 1,179,648 CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296 CONV3-512: [28x28x512] memory: 28*28*512=400K params: (3*3*512)*512 = 2,359,296 POOL2: [14x14x512] memory: 14*14*512=100K params: 0 Most params are CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296 CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296 in late FC CONV3-512: [14x14x512] memory: 14*14*512=100K params: (3*3*512)*512 = 2,359,296 POOL2: [7x7x512] memory: 7*7*512=25K params: 0 FC: [1x1x4096] memory: 4096 params: 7*7*512*4096 = 102,760,448 FC: [1x1x4096] memory: 4096 params: 4096*4096 = 16,777,216 FC: [1x1x1000] memory: 1000 params: 4096*1000 = 4,096,000

TOTAL memory: 24M * 4 bytes ~= 93MB / image (only forward! ~*2 for bwd) TOTAL params: 138M parameters

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Case Study: GoogLeNet

type	patch size/ stride	output size	depth	#1×1	#3×3 reduce	#3×3	#5×5 reduce	#5×5	pool proj	params	ops
convolution	7×7/2	$112 \times 112 \times 64$	1							2.7K	34M
max pool	3×3/2	$56 \times 56 \times 64$	0								
convolution	3×3/1	$56 \times 56 \times 192$	2		64	192				112K	360M
max pool	3×3/2	28×28×192	0								
inception (3a)		$28 \times 28 \times 256$	2	64	96	128	16	32	32	159K	128M
inception (3b)		$28 \times 28 \times 480$	2	128	128	192	32	96	64	380K	304M
max pool	3×3/2	$14 \times 14 \times 480$	0								
inception (4a)		14×14×512	2	192	96	208	16	48	64	364K	73M
inception (4b)		$14 \times 14 \times 512$	2	160	112	224	24	64	64	437K	88M
inception (4c)		$14 \times 14 \times 512$	2	128	128	256	24	64	64	463K	100M
inception (4d)		$14 \times 14 \times 528$	2	112	144	288	32	64	64	580K	119M
inception (4e)		$14 \times 14 \times 832$	2	256	160	320	32	128	128	840K	170M
max pool	3×3/2	7×7×832	0		1					· · · · · · · · · · · · · · · · · · ·	
inception (5a)		7×7×832	2	256	160	320	32	128	128	1072K	54M
inception (5b)		7×7×1024	2	384	192	384	48	128	128	1388K	71M
avg pool	7×7/1	$1 \times 1 \times 1024$	0								
dropout (40%)		1×1×1024	0								
linear		1×1×1000	1) 			1000K	1M
softmax		$1 \times 1 \times 1000$	0								

Fun features:

- Only 5 million params! (Removes FC layers completely)

Compared to AlexNet:

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- 12X less params
- 2x more compute
- 6.67% (vs. 16.4%)

Lecture 7 -

ILSVRC 2015 winner (3.6% top 5 error)



Slide from Kaiming He's recent presentation <u>https://www.youtube.com/watch?v=1PGLj-uKT1w</u>

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(slide from Kaiming He's recent presentation)

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CIFAR-10 experiments



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ILSVRC 2015 winner (3.6% top 5 error)



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(slide from Kaiming He's recent presentation)

Case Study: ResNet

[He et al., 2015]



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- Batch Normalization after every CONV layer
- Xavier/2 initialization from He et al.
- SGD + Momentum (0.9)
- Learning rate: 0.1, divided by 10 when validation error plateaus
- Mini-batch size 256
- Weight decay of 1e-5
- No dropout used



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layer name	output size	18-layer	18-layer 34-layer 50-layer 101-layer			152-layer			
conv1	112×112	7×7, 64, stride 2							
				3×3 max pool, strid	le 2				
conv2_x	56×56	$\left[\begin{array}{c} 3\times3, 64\\ 3\times3, 64 \end{array}\right]\times2$	$\left[\begin{array}{c} 3\times3,64\\ 3\times3,64\end{array}\right]\times3$	$\begin{bmatrix} 1 \times 1, 64 \\ 3 \times 3, 64 \\ 1 \times 1, 256 \end{bmatrix} \times 3$	$\begin{bmatrix} 1 \times 1, 64 \\ 3 \times 3, 64 \\ 1 \times 1, 256 \end{bmatrix} \times 3$	$\begin{bmatrix} 1 \times 1, 64 \\ 3 \times 3, 64 \\ 1 \times 1, 256 \end{bmatrix} \times 3$			
conv3_x	28×28	$\left[\begin{array}{c} 3\times3,128\\ 3\times3,128\end{array}\right]\times2$	$\begin{bmatrix} 3\times3, 128\\ 3\times3, 128 \end{bmatrix} \times 4$	$\begin{bmatrix} 1 \times 1, 128 \\ 3 \times 3, 128 \\ 1 \times 1, 512 \end{bmatrix} \times 4$	$\begin{bmatrix} 1 \times 1, 128 \\ 3 \times 3, 128 \\ 1 \times 1, 512 \end{bmatrix} \times 4$	$\begin{bmatrix} 1 \times 1, 128 \\ 3 \times 3, 128 \\ 1 \times 1, 512 \end{bmatrix} \times 8$			
conv4_x	14×14	$\left[\begin{array}{c} 3\times3,256\\ 3\times3,256\end{array}\right]\times2$	$\begin{bmatrix} 3\times3,256\\3\times3,256\end{bmatrix}\times6$	$\begin{bmatrix} 1 \times 1, 256 \\ 3 \times 3, 256 \\ 1 \times 1, 1024 \end{bmatrix} \times 6$	$\begin{bmatrix} 1 \times 1, 256 \\ 3 \times 3, 256 \\ 1 \times 1, 1024 \end{bmatrix} \times 23$	$\begin{bmatrix} 1 \times 1, 256 \\ 3 \times 3, 256 \\ 1 \times 1, 1024 \end{bmatrix} \times 36$			
conv5_x	7×7	$\left[\begin{array}{c} 3\times3,512\\ 3\times3,512\end{array}\right]\times2$	$\left[\begin{array}{c} 3\times3,512\\ 3\times3,512\end{array}\right]\times3$	$\begin{bmatrix} 1 \times 1, 512 \\ 3 \times 3, 512 \\ 1 \times 1, 2048 \end{bmatrix} \times 3$	$\begin{bmatrix} 1 \times 1, 512 \\ 3 \times 3, 512 \\ 1 \times 1, 2048 \end{bmatrix} \times 3$	$\begin{bmatrix} 1 \times 1, 512 \\ 3 \times 3, 512 \\ 1 \times 1, 2048 \end{bmatrix} \times 3$			
	1×1		av	erage pool, 1000-d fc,	softmax				
FL	OPs	1.8×10^{9}	3.6×10^{9}	3.8×10^{9}	7.6×10^{9}	11.3×10^{9}			

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Case Study Bonus: DeepMind's AlphaGo



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The input to the policy network is a $19 \times 19 \times 48$ image stack consisting of 48 feature planes. The first hidden layer zero pads the input into a 23×23 image, then convolves *k* filters of kernel size 5 × 5 with stride 1 with the input image and applies a rectifier nonlinearity. Each of the subsequent hidden layers 2 to 12 zero pads the respective previous hidden layer into a 21×21 image, then convolves *k* filters of kernel size 3×3 with stride 1, again followed by a rectifier nonlinearity. The final layer convolves 1 filter of kernel size 1×1 with stride 1, with a different bias for each position, and applies a softmax function. The match version of AlphaGo used *k* = 192 filters; Fig. 2b and Extended Data Table 3 additionally show the results of training with *k* = 128, 256 and 384 filters.

policy network:

[19x19x48] Input CONV1: 192 5x5 filters, stride 1, pad 2 => [19x19x192] CONV2..12: 192 3x3 filters, stride 1, pad 1 => [19x19x192] CONV: 1 1x1 filter, stride 1, pad 0 => [19x19] (probability map of promising moves)

Summary

- ConvNets stack CONV,POOL,FC layers
- Trend towards smaller filters and deeper architectures
- Trend towards getting rid of POOL/FC layers (just CONV)
- Typical architectures look like [(CONV-RELU)*N-POOL?]*M-(FC-RELU)*K,SOFTMAX where N is usually up to ~5, M is large, 0 <= K <= 2.
 - but recent advances such as ResNet/GoogLeNet challenge this paradigm
GoogLeNet



GoogLeNet vs State of the art



Zeiler-Fergus Architecture (1 tower)

• **ResNet**. <u>Residual Network</u> developed by Kaiming He et al. was the winner of ILSVRC 2015. It features special *skip* connections and a heavy use of batch normalization. The architecture is also missing fully connected layers at the end of the network. The reader is also referred to Kaiming's presentation (video, slides), and some recent experiments that reproduce these networks in Torch. ResNets are currently by far state of the art Convolutional Neural Network models and are the default choice for using ConvNets in practice (as of May 10, 2016). In particular, also see more recent developments that tweak the original architecture from Kaiming He et al. Identity Mappings in Deep Residual Networks (published March 2016).