Mahendra Singh Meena

Email: mahendra007.s@gmail.com | Homepage: cse.iitkgp.ac.in/~mmeena/

D-339, Rajendra Prasad Hall of Residence
IIT Kharagpur, Kharagpur (W.B.)
Mobile: +91-97355-52560

❖ Educational Qualifications

Degree	School/Institute	Performance	Year
B.Tech + M.Tech Computer Science & Engineering	Indian Institute of Technology, Kharagpur	7.32/10 after 8th Sem	2009-2014
All India Senior School Certificate Examination, CBSE	Kendriya Vidyalaya No.5, Gwalior	89%	2008-2009
All India Secondary School Examination, CBSE	Kendriya Vidyalaya No.5, Gwalior	82%	2006-2007

❖ Technical Skills

Programming Languages	C, JAVA, PHP, Python, HTML, JavaScript, MySQL, Assembly Lang(Intel x86), Socket Programming
Web Development Frameworks/APIs	jQuery, OSClass, Django, APIs : Google Maps, Google Charts, Facebook, Twitter, Flickr
Software Development Packages	Netbeans, GiT, SPIM, LaTeX, Xilinx, Cnet Network Simulator
Operating Systems	Linux (Ubuntu, Fedora, CentOS), Windows (8,7,XP)

❖ Work Experience

Banyan Learning Solutions India Pvt. Ltd. (Bengaluru) — **Summer Internship** — **May-June 2012**

- Developed a web-based administration system for schools, using PHP and MySQL.
- Implemented efficient performance analysis using Google Charts.
- Designed scripting for OCR (Optical Character Recognition) of attendance sheets.
- Code Repository maintenance and merging new features using GiT.

Mozilla Firefox Extension Development — **June 2011**

- Developed an extension (Add-on) Swap Proxy for changing proxy setting quickly with a single click in Mozilla Firefox.
- GUI design using XUL (XML User Interface Language) and basic functioning using JavaScript and XPCOM (cross platform component object model).
  Current Statistics: 559 weekly download and 36,535 Total downloads.

❖ Academic Projects

Energy Efficient Scheduling Policies for Real-Time Embedded Systems (B.Tech Project) — **Feb 2012 – May 2013**

- Analyzed different scheduling policies for both Uniprocessor and Multiprocessor scheduling systems with RT-DVS class of algorithms.
- Proposed a modified version of Pfair scheduling with lower switching overheads and integration possibility of RT-DVS heuristics for reducing power consumption.

Implementation of combinatorial algorithm to find the orthogonal hull of an object (Design Lab.) — **Aug 2013 – Oct 2013**

- Java applet implementation of linear time combinatorial algorithm to find orthogonal convex hull of an object to be drawn by user.
- Added preferences/settings for user interface customization.

Google Map Traveler (Web-Mashup) — **Apr 2013**

- Developed a real-time web application to fetch tweets and Flickr photos for a location selected by user on Google map.
- Integrated Twitter and Flickr APIs with Google Maps API for proper functioning.

File System Implementation — **Apr 2012**

- Design and implementation of File system and integration with FUSE as final project for Operating Systems Lab.

Spanning Tree Protocol simulation for Bridged Ethernet Local Network — **Apr 2012**

- Developed a simulator for spanning tree protocol for bridge functioning using cnet network simulator framework as final project for Computer Networks Lab.

Tiny Compiler — **Nov 2011**

- Developed a tiny compiler for basic assembly code generation for python-type language in C as final project for Compilers Lab.

❖ Scholastic Achievements

- Qualified for the award of scholarship under NTSE (National Talent Search Examination), 2007 conducted by NCERT (National Council for Educational Research and Training).
- Secured 2nd position in GK Quiz competition at regional level organized during Regional level Social Science Exhibition, 2005.