SAFE & SECURE
INTELLIGENT SYSTEM

VLSI SOCIETY OF INDIA

31st
International Conference
on VLSI Design

VLSi DESigN 2018
ON EMBEDDED SYSTEMS

17th
International Conference
on Embedded Systems

January, 6th - 10th 2018, Pune, India

Certificate of Participation

This is to certify that,

Kunal Banarjee - Best Thesis Winner (PhD Forum)

has presented the paper titled "Translation Varidation of optimization Transformation of programs using equivalence checking " at the 31st International Conference On VLSI Design and 17th International Conference On Embedded System, held from 6th to 10th Jan 2018 in Pune, India.

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