

FPGA PROTOTYPING Using VERILOG

25th to 30th June 2012

REGISTRATION FORM

1. Name :.....
2. Designation & Deptt. :.....
3. Address :.....
.....
.....
4. Phone (Mob.) :.....
Phone (Resi.) :.....
E-mail (compulsory) :.....
5. Male/Female :.....
6. Date of Birth :.....
7. Highest Academic Qualification:.....
8. Accommodation Required (Y/N):.....

For AICTE Sponsored Candidates:

Recommended and Forwarded :

Date:

Signature and Seal of the
Head of the Organization

For Non-AICTE Candidates:

1. Demand draft of Rs. _____ no. _____
dated ____/____/____/ drawn on _____
_____ (Bank) in favour of "CEP-STC, IIT Kharagpur" payable
at Kharagpur.
2. Forwarding letter from the HOD/College/Institute proving Studentship:

Date :

Place :

Signature of the Applicant