

Channel Systems

Lecture #4 of Model Checking

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Overview Lecture #4

- What is a channel system?
- Example: alternating bit protocol
- From channel systems to transition systems
- The modeling language `NanoPromela`
- Examples
- Semantics of `NanoPromela` models

Channels

- Processes communicate via *channels* ($c \in Chan$)
- *Channels* are first-in, first-out buffers
- *Channels* are typed (wrt. their content — $dom(c)$)
- *Channels* buffer messages (of appropriate type)
- *Channel capacity* = maximum # messages that can be stored
 - if $cap(c) \in \mathbb{N}$ then c is a channel with finite capacity
 - if $cap(c) = \infty$ then c has an infinite capacity
 - if $cap(c) > 0$, there is some “delay” between sending and receipt
 - if $cap(c) = 0$, then communication via c amounts to *handshaking*

Channels

- Process $P_i = \text{program graph } PG_i + \text{communication actions}$

$c!e$ transmit the value of expression e along channel c

$c?x$ receive a message via channel c and assign it to variable x

- $Comm = \{ c!e, c?x \mid c \in Chan, e \in Expr, x \in Var. \text{ dom}(x) \supseteq \text{dom}(c) = \text{dom}(e) \}$
- Sending and receiving a message
 - $c!e$ puts the value of e at the rear of the buffer c (if c is not full)
 - $c?x$ retrieves the front element of the buffer and assigns it to x (if c is not empty)
 - if $cap(c) = 0$, channel c has *no* buffer
 - if $cap(c) = 0$, sending and receiving can take place simultaneously
this is called *synchronous message passing* or *handshaking*
 - if $cap(c) > 0$, sending and receiving can never take place simultaneously
this is called *asynchronous message passing*

Channel systems

A **program graph** over $(Var, Chan)$ is a tuple

$$PG = (Loc, Act, Effect, \rightarrow, Loc_0, g_0)$$

where

$$\rightarrow \subseteq Loc \times Cond(Var) \times (Act \cup Comm) \times Loc$$

A **channel system** CS over $(\bigcup_{0 < i \leq n} Var_i, Chan)$:

$$CS = [PG_1 \mid \dots \mid PG_n]$$

with program graphs PG_i over $(Var_i, Chan)$

Communication actions

- *Handshaking*

- if $\text{cap}(c) = 0$, then process P_i can perform $\ell_i \xrightarrow{c!e} \ell'_i$ only
- . . . if P_j , say, can perform $\ell_j \xrightarrow{c?x} \ell'_j$
- the effect corresponds to the (atomic) *distributed* assignment $x := \text{value}(e)$

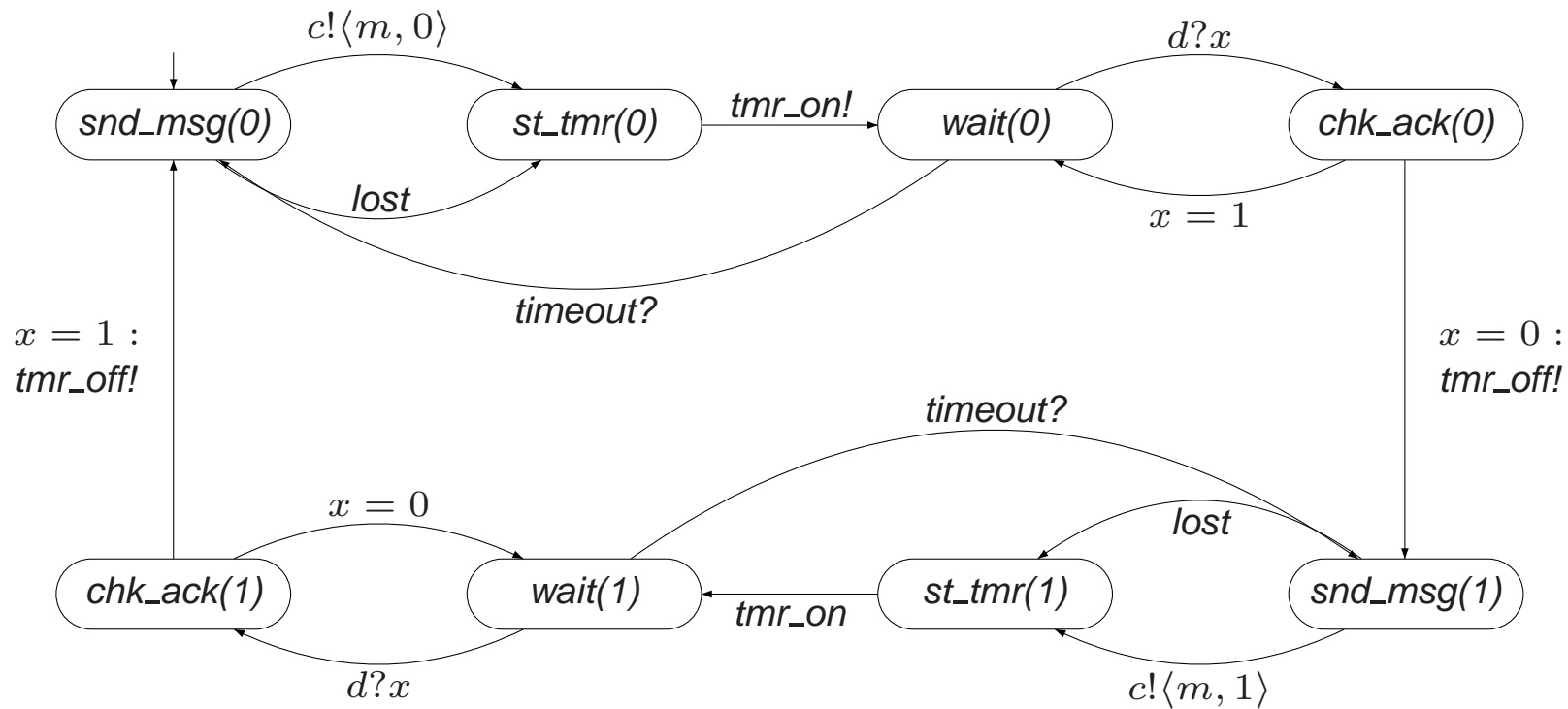
- *Asynchronous message passing*

- if $\text{cap}(c) > 0$, then process P_i can perform $\ell_i \xrightarrow{c!e} \ell'_i$
- . . . if and only if less than $\text{cap}(c)$ messages are stored in c
- P_j may perform $\ell_j \xrightarrow{c?x} \ell'_j$ if and only if the buffer of c is not empty
- then the first element of the buffer is extracted and assigned to x (*atomically*)

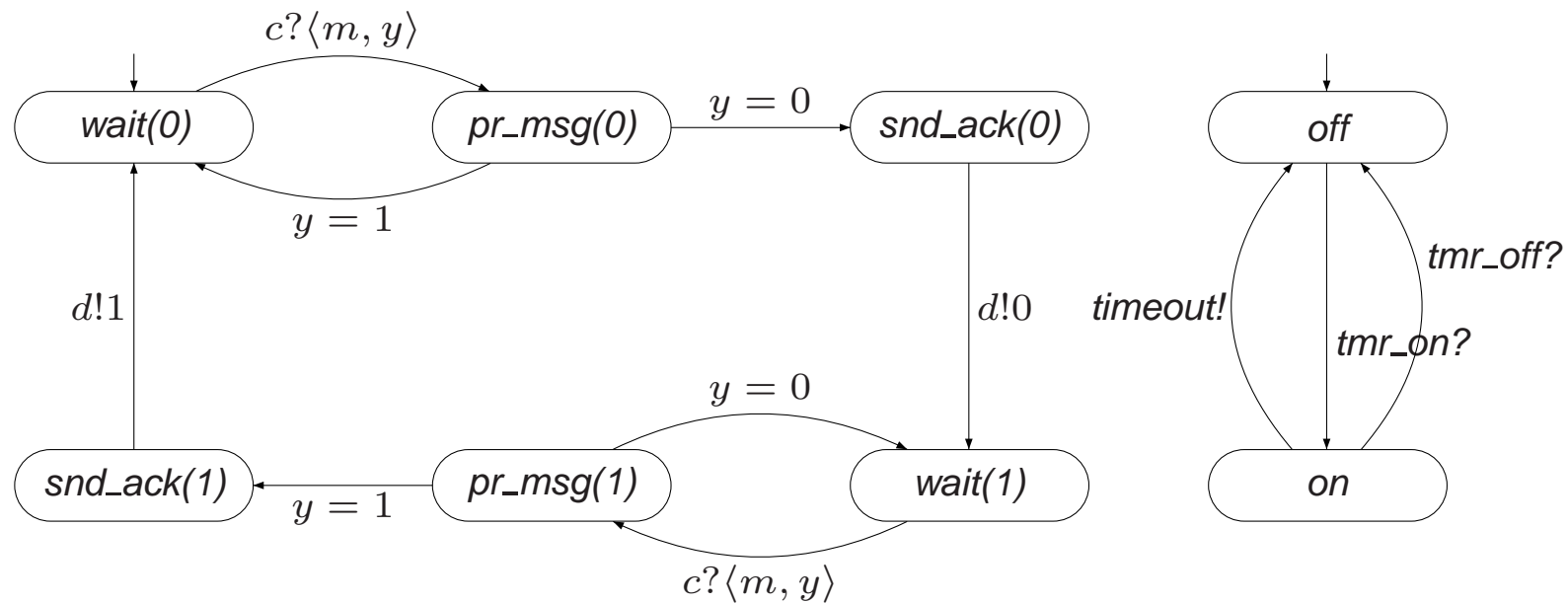
	executable if . . .	effect
$c!e$	c is not “full”	$\text{Enqueue}(c, \text{value}(e))$
$c?x$	c is not empty	$\langle x := \text{Front}(c) ; \text{Dequeue}(c) \rangle;$

The alternating bit protocol

The alternating bit protocol: sender



The alternating bit protocol: receiver



Channel evaluations

- A *channel evaluation* ξ is
 - a mapping from channel $c \in Chan$ onto a sequence $\xi(c) \in dom(c)^*$ such that
 - current length cannot exceed the capacity of c : $len(\xi(c)) \leq cap(c)$
 - $\xi(c) = v_1 v_2 \dots v_k$ ($cap(c) \geq k$) denotes v_1 is at front of buffer etc.
- $\xi[c := v_1 \dots v_k]$ denotes the channel evaluation

$$\xi[c := v_1 \dots v_k](c') = \begin{cases} \xi(c') & \text{if } c \neq c' \\ v_1 \dots v_k & \text{if } c = c'. \end{cases}$$

- Initial channel evaluation ξ_0 equals $\xi_0(c) = \varepsilon$ for any c

Transition system semantics of a channel system

Let $CS = [PG_1 \mid \dots \mid PG_n]$ be a *channel system* over $(Chan, Var)$ with

$$PG_i = (Loc_i, Act_i, Effect_i, \rightsquigarrow_i, Loc_{0,i}, g_{0,i}), \quad \text{for } 0 < i \leq n$$

$TS(CS)$ is the *transition system* $(S, Act, \rightarrow, I, AP, L)$ where:

- $S = (Loc_1 \times \dots \times Loc_n) \times Eval(Var) \times Eval(Chan)$
- $Act = (\biguplus_{0 < i \leq n} Act_i) \uplus \{ \tau \}$
- \rightarrow is defined by the inference rules on the next slides
- $I = \left\{ \langle \ell_1, \dots, \ell_n, \eta, \xi_0 \rangle \mid \forall i. (\ell_i \in Loc_{0,i} \wedge \eta \models g_{0,i}) \wedge \forall c. \xi_0(c) = \varepsilon \right\}$
- $AP = \biguplus_{0 < i \leq n} Loc_i \uplus Cond(Var)$
- $L(\langle \ell_1, \dots, \ell_n, \eta, \xi \rangle) = \{ \ell_1, \dots, \ell_n \} \cup \{ g \in Cond(Var) \mid \eta \models g \}$

Inference rules (I)

- Interleaving for $\alpha \in \text{Act}_i$:

$$\frac{\ell_i \xrightarrow{g:\alpha} \ell'_i \quad \wedge \quad \eta \models g}{\langle \ell_1, \dots, \ell_i, \dots, \ell_n, \eta, \xi \rangle \xrightarrow{\alpha} \langle \ell_1, \dots, \ell'_i, \dots, \ell_n, \eta', \xi \rangle}$$

where $\eta' = \text{Effect}(\alpha, \eta)$

- Synchronous message passing over $c \in \text{Chan}$, $\text{cap}(c) = 0$:

$$\frac{\ell_i \xrightarrow{g:c?x} \ell'_i \quad \wedge \quad \ell_j \xrightarrow{g':c!e} \ell'_j \quad \wedge \quad \eta \models g \wedge g' \quad \wedge \quad i \neq j}{\langle \ell_1, \dots, \ell_i, \dots, \ell_j, \dots, \ell_n, \eta, \xi \rangle \xrightarrow{\tau} \langle \ell_1, \dots, \ell'_i, \dots, \ell'_j, \dots, \ell_n, \eta', \xi \rangle}$$

where $\eta' = \eta[x := \eta(e)]$.

Inference rules (II)

- Asynchronous message passing for $c \in \text{Chan}$, $\text{cap}(c) > 0$:
 - receive a value along channel c and assign it to variable x :

$$\frac{\ell_i \xrightarrow{g:c?x} \ell'_i \wedge \eta \models g \wedge \text{len}(\xi(c)) = k > 0 \wedge \xi(c) = v_1 \dots v_k}{\langle \ell_1, \dots, \ell_i, \dots, \ell_n, \eta, \xi \rangle \xrightarrow{\tau} \langle \ell_1, \dots, \ell'_i, \dots, \ell_n, \eta', \xi' \rangle}$$

where $\eta' = \eta[x := v_1]$ and $\xi' = \xi[c := v_2 \dots v_k]$.

- transmit value $\eta(e) \in \text{dom}(c)$ over channel c :

$$\frac{\ell_i \xrightarrow{g:c!e} \ell'_i \wedge \eta \models g \wedge \text{len}(\xi(c)) = k < \text{cap}(c) \wedge \xi(c) = v_1 \dots v_k}{\langle \ell_1, \dots, \ell_i, \dots, \ell_n, \eta, \xi \rangle \xrightarrow{\tau} \langle \ell_1, \dots, \ell'_i, \dots, \ell_n, \eta, \xi' \rangle}$$

where $\xi' = \xi[c := v_1 v_2 \dots v_k \eta(e)]$.

Handling unexpected messages

sender S	timer	receiver R	channel c	channel d	event
$snd_msg(0)$	off	$wait(0)$	\emptyset	\emptyset	message with bit 0 transmitted
$st_tmr(0)$	off	$wait(0)$	$\langle m, 0 \rangle$	\emptyset	
$wait(0)$	on	$wait(0)$	$\langle m, 0 \rangle$	\emptyset	
$snd_msg(0)$	off	$wait(0)$	$\langle m, 0 \rangle$	\emptyset	timeout
$st_tmr(0)$	off	$wait(0)$	$\langle m, 0 \rangle \langle m, 0 \rangle$	\emptyset	retransmission
$st_tmr(0)$	off	$pr_msg(0)$	$\langle m, 0 \rangle$	\emptyset	receiver reads first message
$st_tmr(0)$	off	$snd_ack(0)$	$\langle m, 0 \rangle$	\emptyset	receiver changes into mode-1
$st_tmr(0)$	off	$wait(1)$	$\langle m, 0 \rangle$	0	
$st_tmr(0)$	off	$pr_msg(1)$	\emptyset	0	
$st_tmr(0)$	off	$wait(1)$	\emptyset	0	receiver reads retransmission and ignores it
\vdots	\vdots	\vdots	\vdots	\vdots	

nanoPromela

- **Promela** (Process Meta Language) is modeling language for SPIN
 - SPIN = most widely used model checker
 - developed by Gerard Holzmann (Bell Labs, NASA JPL)
 - ACM Software Award 2002
- **nanoPromela** is the core of Promela
 - shared variables and channel-based communication
 - formal semantics of a Promela model is a channel system
 - processes are defined by means of a guarded command language
- No actions, statements describe effect of actions

nanoPromela

nanoPromela-program $\overline{\mathcal{P}} = [\mathcal{P}_1 | \dots | \mathcal{P}_n]$ with \mathcal{P}_i a process

A **process** is specified by a statement:

$$\begin{aligned} \text{stmt} & ::= \text{skip} \mid x := \text{expr} \mid c?x \mid c!\text{expr} \mid \\ & \quad \text{stmt}_1; \text{stmt}_2 \mid \text{atomic}\{\text{assignments}\} \mid \\ & \quad \mathbf{if} \quad :: g_1 \Rightarrow \text{stmt}_1 \quad \dots \quad :: g_n \Rightarrow \text{stmt}_n \quad \mathbf{fi} \quad | \\ & \quad \mathbf{do} \quad :: g_1 \Rightarrow \text{stmt}_1 \quad \dots \quad :: g_n \Rightarrow \text{stmt}_n \quad \mathbf{do} \\ \text{assignments} & ::= x_1 := \text{expr}_1; x_2 := \text{expr}_2; \dots; x_m := \text{expr}_m \end{aligned}$$

x is a variable in *Var*, *expr* an expression and c a channel, g_i a guard

assume the Promela specification is type-consistent

Conditional statements

if $:: g_1 \Rightarrow \text{stmt}_1 \dots :: g_n \Rightarrow \text{stmt}_n$ **fi**

- Nondeterministic choice between statements stmt_i for which g_i holds
- Test-and-set semantics: (deviation from Promela)
 - guard evaluation + selection of enabled command + execution first atomic step of selected statement is all performed **atomically**
- The **if-fi**-command **blocks** if no guard holds
 - parallel processes may unblock a process by changing shared variables
 - e.g., when $y=0$, **if** $:: y > 0 \Rightarrow x := 42$ **fi** waits until y exceeds 0
- Standard abbreviations:
 - **if** g **then** stmt_1 **else** stmt_2 **fi** \equiv **if** $:: g \Rightarrow \text{stmt}_1 :: \neg g \Rightarrow \text{stmt}_2$ **fi**
 - **if** g **then** stmt_1 **fi** \equiv **if** $:: g \Rightarrow \text{stmt}_1 :: \neg g \Rightarrow \text{skip}$ **fi**

Iteration statements

do $:: g_1 \Rightarrow \text{stmt}_1 \dots :: g_n \Rightarrow \text{stmt}_n$ **od**

- Iterative execution of nondeterministic choice among $g_i \Rightarrow \text{stmt}_i$
 - where guard g_i holds in the current state
- No blocking if all guards are violated; instead, loop is aborted
- **do** $:: g \Rightarrow \text{stmt}$ **od** \equiv **while** g **do** stmt **od**
- No break-statements to abort a loop (deviation from Promela)

Peterson's algorithm

The nanoPromela-code of process \mathcal{P}_1 is given by the statement:

```
do  :: true  $\Rightarrow$  skip;  
      atomic{ $b_1 := \text{true}; x := 2$ };  
      if  ::  $(x = 1) \vee \neg b_2 \Rightarrow \text{crit}_1 := \text{true}$  fi  
      atomic{ $\text{crit}_1 := \text{false}; b_1 := \text{false}$ }  
od
```

Beverage vending machine

The following nanoPromela program describes its behaviour:

```
do  :: true  $\Rightarrow$ 
      skip;
      if      :: nsprite > 0  $\Rightarrow$  nsprite := nsprite - 1
              :: nbeer > 0  $\Rightarrow$  nbeer := nbeer - 1
              :: nsprite = nbeer = 0  $\Rightarrow$  skip
      fi
  :: true  $\Rightarrow$  atomic{nbeer := max; nsprite := max}
od
```

Formal semantics

The *semantics* of a nanoPromela-statement over $(Var, Chan)$ is a *program graph* over $(Var, Chan)$.

The program graphs PG_1, \dots, PG_n for the processes $\mathcal{P}_1, \dots, \mathcal{P}_n$ of a nanoPromela-program $\overline{\mathcal{P}} = [\mathcal{P}_1 | \dots | \mathcal{P}_n]$ constitute a *channel system* over $(Var, Chan)$

Example:

```
loop  =  do  ::   $x > 1 \Rightarrow y := x + y$ 
          ::   $y < x \Rightarrow x := 0; y := x$ 
        od
```

Sub-statements

Inference rules

$$\frac{}{\text{skip} \xrightarrow{\text{true: } id} \text{exit}}$$

where id denotes an action that does not change the values of the variables

$$\frac{}{x := \text{expr} \xrightarrow{\text{true: assign}(x, \text{expr})} \text{exit}}$$

$\text{assign}(x, \text{expr})$ denotes the action that only changes x , no other variables

$$\frac{}{c?x \xrightarrow{\text{true: } c?x} \text{exit}}$$

$$\frac{}{c!\text{expr} \xrightarrow{\text{true: } c!\text{expr}} \text{exit}}$$

Inference rules

$$\frac{}{\text{atomic}\{x_1 := \text{expr}_1; \dots; x_m := \text{expr}_m\} \xrightarrow{\text{true} : \alpha_m} \text{exit}}$$

where $\alpha_0 = id$, $\alpha_i = \text{Effect}(\text{assign}(x_i, \text{expr}_i), \text{Effect}(\alpha_{i-1}, \eta))$ for $1 \leq i \leq m$

$$\frac{\text{stmt}_1 \xrightarrow{g:\alpha} \text{stmt}'_1 \neq \text{exit}}{\text{stmt}_1; \text{stmt}_2 \xrightarrow{g:\alpha} \text{stmt}'_1; \text{stmt}_2}$$

$$\frac{\text{stmt}_1 \xrightarrow{g:\alpha} \text{exit}}{\text{stmt}_1; \text{stmt}_2 \xrightarrow{g:\alpha} \text{stmt}_2}$$

Inference rules

$$\frac{\text{stmt}_i \xrightarrow{h:\alpha} \text{stmt}'_i}{\text{cond_cmd} \xrightarrow{g_i \wedge h:\alpha} \text{stmt}'_i}$$

$$\frac{\text{stmt}_i \xrightarrow{h:\alpha} \text{stmt}'_i \neq \text{exit}}{\text{loop} \xrightarrow{g_i \wedge h:\alpha} \text{stmt}'_i; \text{loop}} \quad \frac{\text{stmt}_i \xrightarrow{h:\alpha} \text{exit}}{\text{loop} \xrightarrow{g_i \wedge h:\alpha} \text{loop}}$$

$$\frac{}{\text{loop} \xrightarrow{\neg g_1 \wedge \dots \wedge \neg g_n} \text{exit}}$$