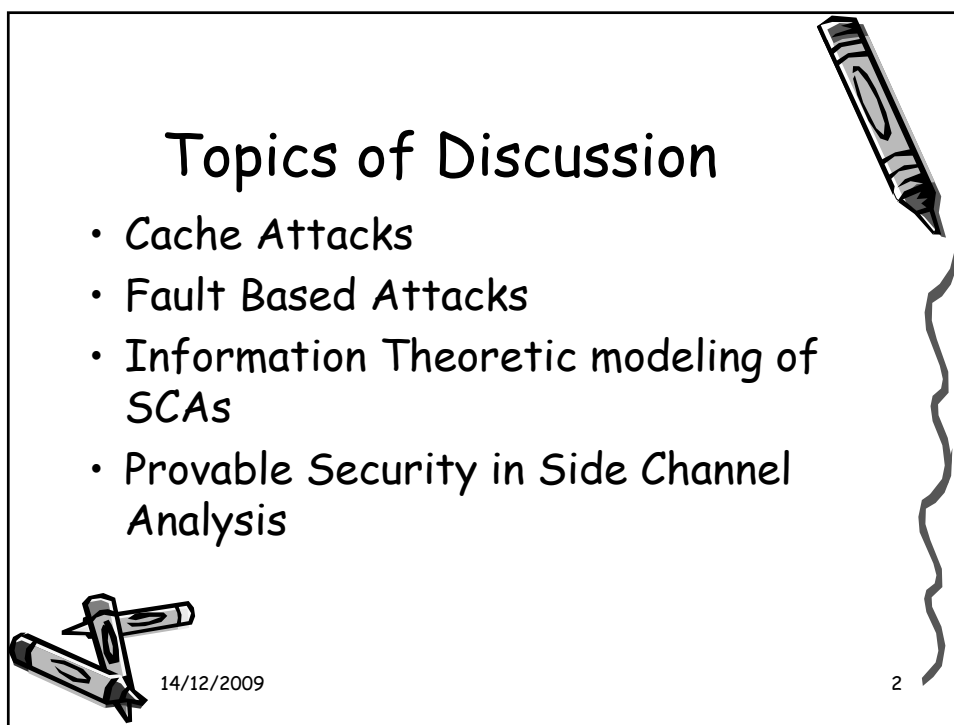



Side Channels in Cryptography-Part II

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Topics of Discussion

- Cache Attacks
- Fault Based Attacks
- Information Theoretic modeling of SCAs
- Provable Security in Side Channel Analysis



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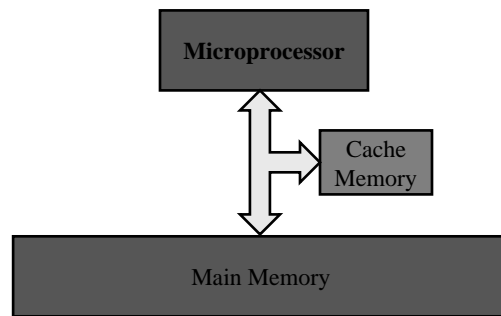
Cache Attacks



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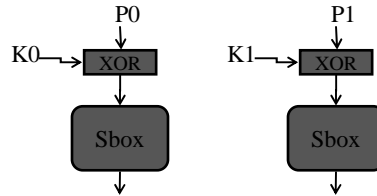
Cache Memory Leaks Information



- | | |
|---|--|
| <ul style="list-style-type: none">• If there is a <i>Cache Hit</i><ul style="list-style-type: none">- Access time is less- Power Consumption is less | <ul style="list-style-type: none">• If there is a <i>Cache Miss</i><ul style="list-style-type: none">- Access time is more- Power Consumption is more |
|---|--|

4

Cache Attacks : The Principle



- Power and Time for the $(P1 \wedge K1)$ depends on the previous sbox access.

- If cache hit :

- $(P0 \wedge K0) = (P1 \wedge K1)$

- $\Rightarrow (K0 \wedge K1) = (P0 \wedge P1)$

Since we know $P0$ and $P1$, we can determine $(K0 \wedge K1)$.

...but we need to differentiate between a cache hit and miss.

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Classes of Cache Attacks

Three ways to identify cache behavior

- Cache Trace Attacks
- Cache Access Attacks
- Cache Timing Attacks

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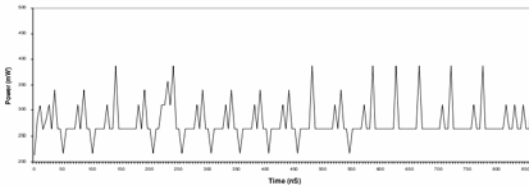
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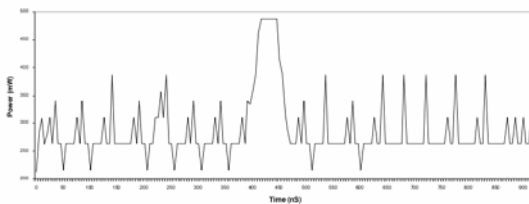
Cache Trace Attacks

- The Power Profile of the system gives away cache behavior.

Without Cache Miss



With Cache Miss

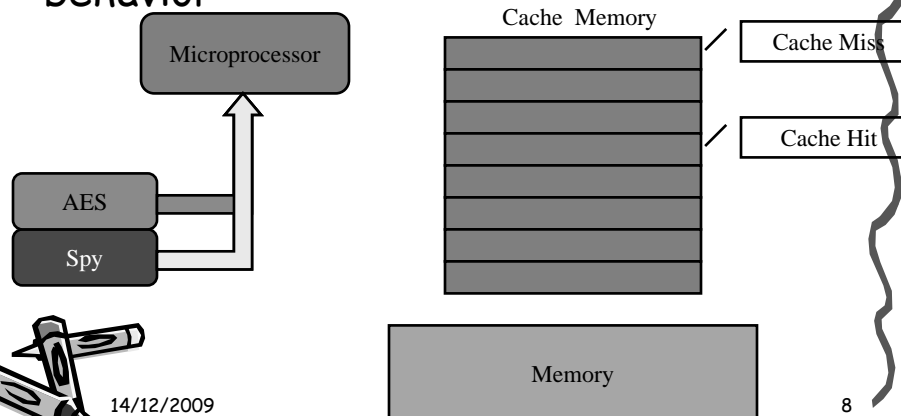


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Cache Access Attacks : Osvik's Attack

- Uses a spy program to determine cache behavior



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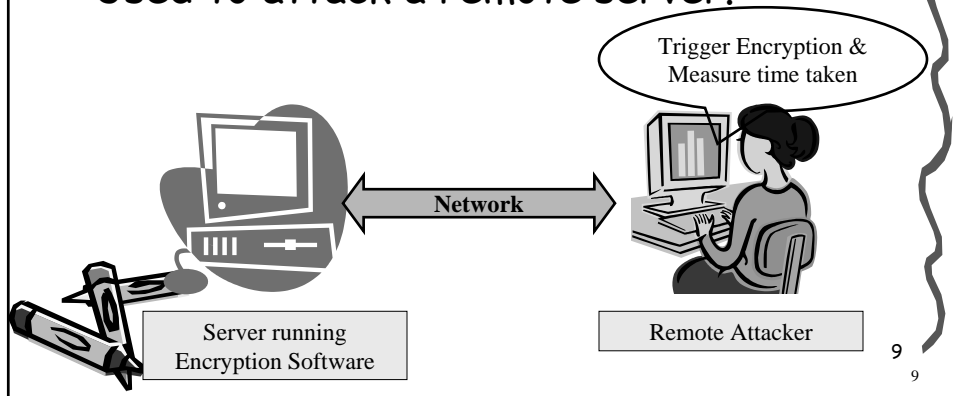
8

Cache Timing Attack

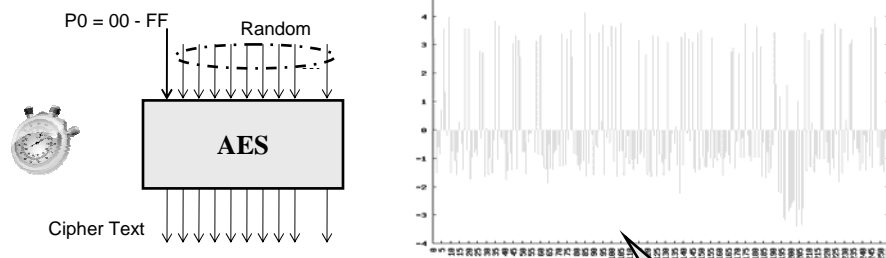
- Based on measuring the total time for encryption.

$$\text{Execution Time} \approx N_h * T_h + N_m * T_m + K$$

- Used to attack a remote server.



Bernstein's Cache Timing Experiment



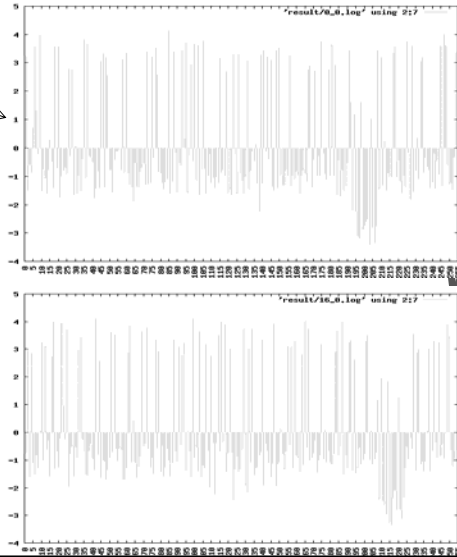
- For P0 = 0 to 255
 - For 2¹⁵ iterations
 - Vary remaining plaintext by
 - AES Encrypt
 - Determine Time For Encryption

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Bernstein's Cache Timing Attack

- Put key as all ZEROs and perform experiment
- Repeat experiment with unknown key
- Correlate the two results



Why Cache Attacks Work on AES

- The AES Structure
 - Add initial Key
 - Nine Rounds of
 - Sub Byte
 - Shift Row
 - Mix Column
 - Add Round Key
 - Final Round
 - Sub Byte
 - Shift Row
 - Add Round Key

Substitute 16 bytes from a 256 byte lookup table

Chester Rebeiro, Mainack Mandal, Debdeep Mukhopadhyay, "Pinpointing Cache Timing Attacks on AES", To appear in the 23rd International Conference on VLSI Design and 9th International Conference on Embedded Systems, VLSID 2010, Bangalore, India.

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Software Implementations of AES (OpenSSL)

- Merges all round operations into 4 lookups.
- Uses 4 tables T0, T1, T2, T3, each of size 1024 byte.
- The Software Structure is

Round 1

```

a0 =T0[b024] ^ T1[b116] ^ T2[b28] ^ T4[b30] ^ rk[4]
a1 =T0[b124] ^ T1[b216] ^ T2[b38] ^ T4[b00] ^ rk[5]
a2 =T0[b224] ^ T1[b316] ^ T2[b08] ^ T4[b10] ^ rk[6]
a3 =T0[b324] ^ T1[b016] ^ T2[b18] ^ T4[b20] ^ rk[7]
    
```

Round 2

```

b0 =T0[a024] ^ T1[a116] ^ T2[a28] ^ T4[a30] ^ rk[8]
b1 =T0[a124] ^ T1[a216] ^ T2[a38] ^ T4[a00] ^ rk[9]
b2 =T0[a224] ^ T1[a316] ^ T2[a08] ^ T4[a10] ^ rk[10]
b3 =T0[a324] ^ T1[a016] ^ T2[a18] ^ T4[a20] ^ rk[11]
    
```

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AES Execution Time for 4KB Table

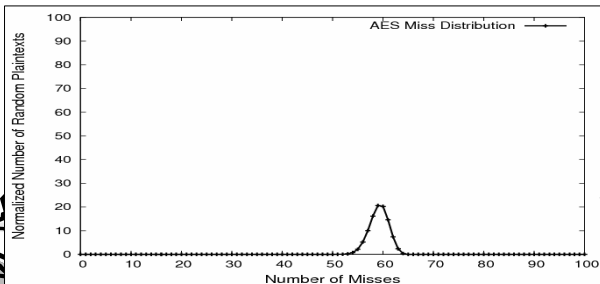
- Encryption Time(E) $\approx N_h * T_h + N_m * T_m + K$

Since, $N_t = N_m + N_h$

$$E = (N_t - N_m) T_h + N_m * T_m + K$$

$$= N_t * T_h + (T_m - T_h) N_m + K$$

- Time(E2) - Time(E1) = $a N_m$

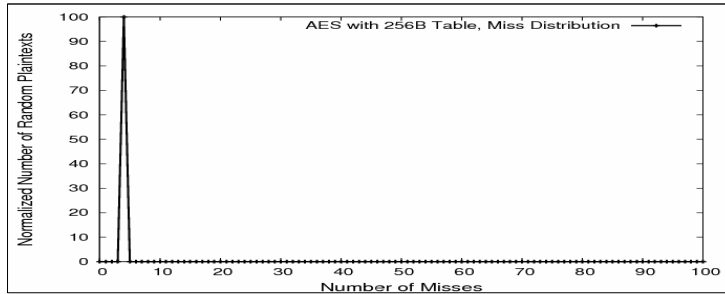


Cache Attack works because of varying execution time.

14

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AES Execution Time with 256Byte Table



- Δ Encryption Time = aNm
 - $Nm = \text{TableSize}/\text{CacheLineSize}$
 $= 256/64 = 4$
- Encryption Time is $a \cdot 4$

Cache Attacks
Believed to be not
possible

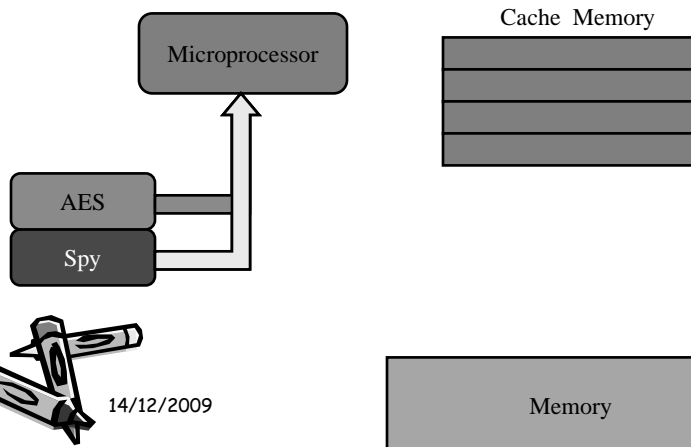


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Osvik's Attack on AES-with 256B Table

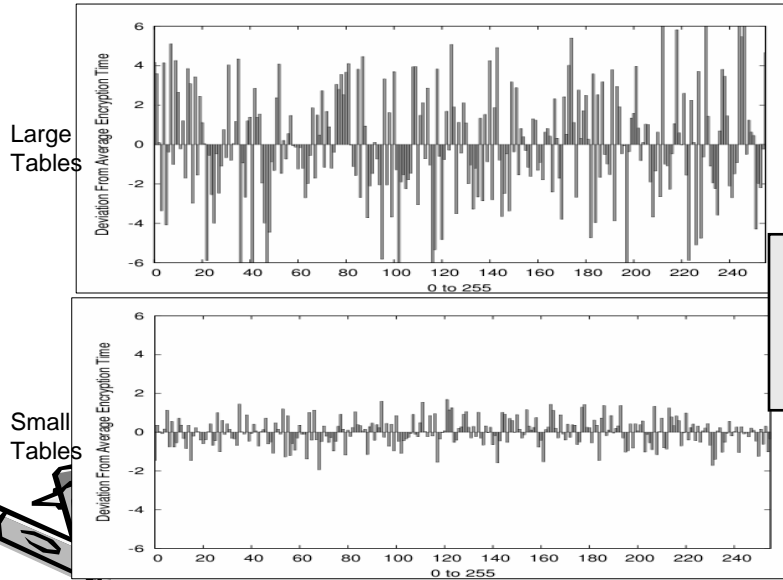
- Won't Work!!!



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Bernstein's Attack on Small Tables



Lesser
deviations in
Encryption
Time

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Some Opinions on Cache Timing Attacks

[Kelsey. et.al., 2000, Section 7]

We believe attacks based on cache hit ratio in large Sbox ciphers like Blowfish, CAST and Khufu are possible

We now show that cache attacks are possible even on ciphers with small sboxes

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Modern Cache Memory

- Processor's aim : Reduce the miss penalty
- Techniques used to achieve this are
 - Speculative Loading
 - Prefetching
 - Out-of-order loading
 - Parallelization
 - Overlapping



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Speculative Loading & Prefetching

- ***Speculative Loading*** : Loading of data into processor before preceding branches are resolved.
 - ***Effect on Cipher Execution : None, since branches are generally not used in block ciphers.***
- ***Prefetching*** : data to be loaded into cache when processor detects memory accesses in a sequential order.
 - ***Effect on Cipher Execution : None, since memory accesses are random in nature.***



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Out-of-Order Loading

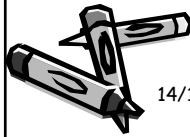
- Data accessed in an order not strictly specified by the program.

Program

LOAD AX, #1000
LOAD BX, #2000
LOAD CX, #3000
LOAD DX, #4000

Execution

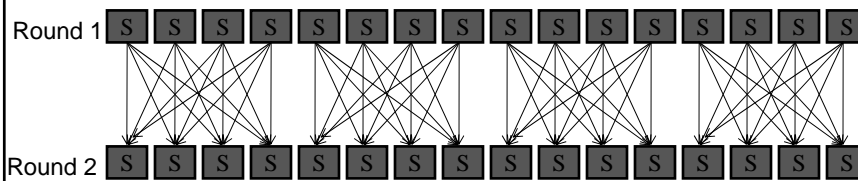
LOAD DX, #4000
LOAD BX, #2000
LOAD AX, #1000
LOAD CX, #3000



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Out-of-Order Loading on a Cipher



Out of order loading works only within a round & not between rounds.



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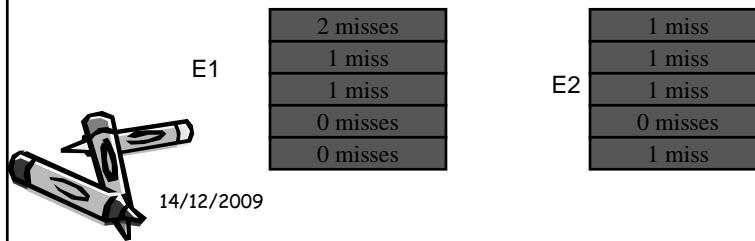
22
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Parallelization & Overlapping

- **Parallelization** : Two or more cache misses are serviced simultaneously.
- **Overlapping** : Memory reads are pipelined.

Effect on Cipher Execution :

- Consider a 5 round block cipher with 4 misses
- $Time(E1) < Time(E2)$



On Modern Caches

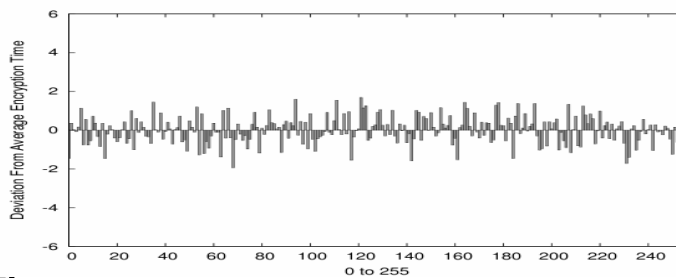
- Execution Time not only depends on the number of misses, but also on the distribution of misses.
- In fact, our findings show that execution time is inversely proportional to the number of misses in the first round.
- Distribution of misses depends on the key, so, execution time depends on the key.



But, why doesn't Bernstein's Attack work on small table AES?

Because,

1. the deviation in encryption time is too less.
2. Bernstein's timing measurements



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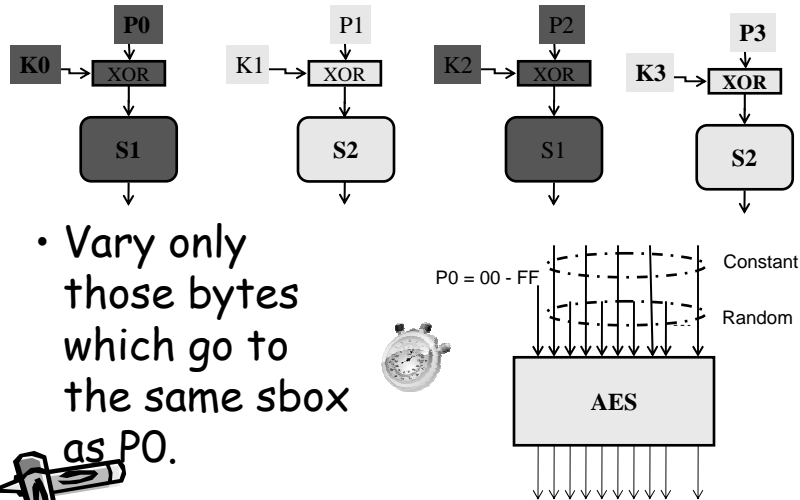
Modification's to Bernstein's Attack(1)

- Improving Timing Measurements
 - Bernstein used 'RDTSC' instruction to measure timing.
 - This is prone to errors due to processor pipeline and out-of-order execution.
 - Instead use 'CUID' before calling RDTSC
 - This flushes the processor pipeline.
 - Hence all RDTSC instructions done on a fresh pipeline.

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Modification's to Bernstein's Attack(2)



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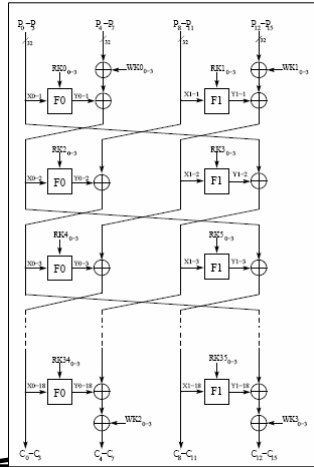
Attack of Clefia

- Clefia is a block cipher designed by Sony Corporations
- It has small tables of 256 bytes.
- There are 18 rounds in the cipher
 - each round has 8 look-ups
 - there are 2 S Boxes of 256 bytes
 - Thus there are 4 look-ups per S-box in each round.

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Clelia Attack Results



- In around 2^{26} Clelia encryptions the cipher can be shown to break in the face of cache timing attacks
- 3 GHz Intel Core 2 Duo
- 32 kB L1 Cache
- 1 GB RAM
- Linux (Ubuntu 8.04)
- gcc -4.2.4 with O3 optimization.
- Attack Time:
 - First Phase (with known key): 1300 sec
 - Second Phase (with unknown key): 312.5 sec

Chester Rebeiro, Debdeep Mukhopadhyay, Junko Takahashi and Toshinori Fukunaga, "Cache Timing Attacks on Clelia", To appear in Indocrypt 2009.

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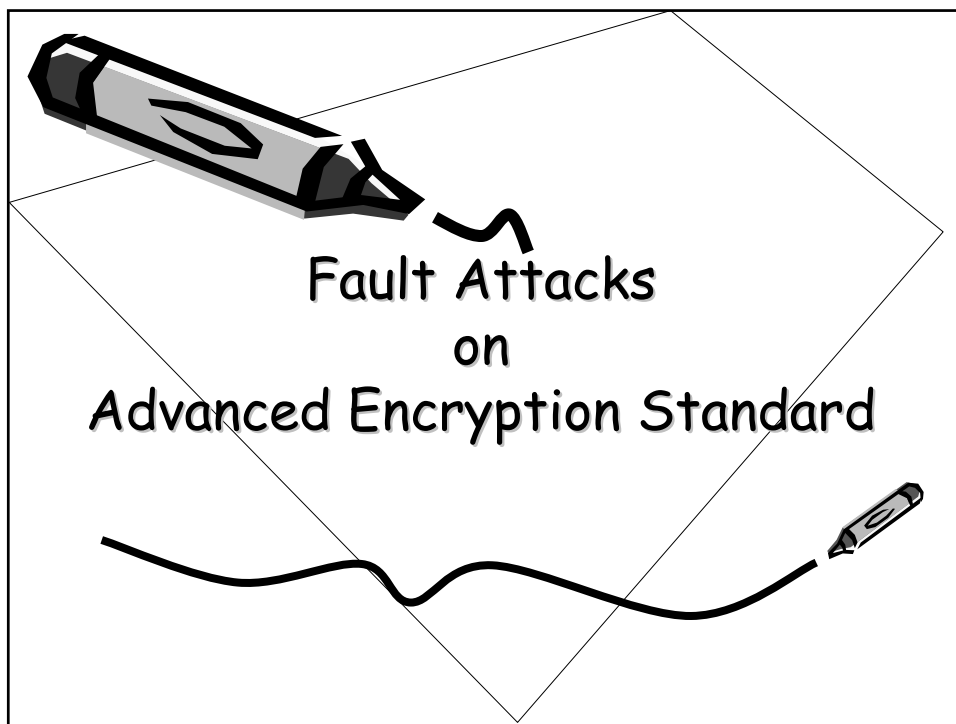
Correlation Results on CLEFIA

Key Byte	Correct Key	Obtained Correlation results (with correlation value)
RK_{0_0}	0a	0a(884.6), 6b(469.7), 5f(368.3), 20(357.3), ef(263.7) ...
RK_{0_1}	96	96(1853.4), 7b(438.0), bc(437.5), 4a(366.7), ee(361.8) ...
RK_{0_2}	c1	c1(1942.1), 93(672.7), 98(598.3), f9(573.2), 24(559.5) ...
RK_{0_3}	68	68(1680.3), 23(415.9), 9e(414.1), 6e(398.9), 99(375.9) ...
RK_{1_0}	ac	ac(4077.6), c1(853.4), 11(843.5), 7c(650.9), 71(639.2) ...
RK_{1_1}	b0	b0(3089.8), 73(740.8), 07(716.7), f7(677.1), 01(658.1) ...
RK_{1_2}	7a	7a(5721.0), 0a(1539.1), 08(1230.2), 6f(967.8), 05(931.3) ...
RK_{1_3}	79	79(5361.6), fb(1202.0), 2b(1196.0), 9a(1106.6), 07(1007.9) ...
$RK_{2_0} \oplus WK_{0_0}$	6e	6e(4194.0), f9(1526.2), 07(1491.3), 96(1257.9), 2f(1194.3) ...
$RK_{2_1} \oplus WK_{0_1}$	b1	b1(4344.0), 39(1197.5), 59(1056.8), 63(980.9), f9(926.9) ...
$RK_{2_2} \oplus WK_{0_2}$	9f	9f(2662.0), d4(1327.9), 68(1071.1), 1b(1056.2), 89(1000.0) ...
$RK_{2_3} \oplus WK_{0_3}$	61	61(6840.2), 0a(1783.8), 97(1587.3), 8c(1555.8), 87(1491.4) ...
$RK_{3_0} \oplus WK_{1_0}$	c3	c3(21042.8), 38(4644.1), ea(4429.9), d3(3999.8), 01(3995.1) ...
$RK_{3_1} \oplus WK_{1_1}$	85	85(34258.3), 7d(8695.1), 83(8576.9), 3a(8401.3), ec(8318.5) ...
$RK_{3_2} \oplus WK_{1_2}$	2c	2c(37773.2), 3c(7131.3), 28(6804.1), 05(6263.3), b5(5906.3) ...
$RK_{3_3} \oplus WK_{1_3}$	4d	4d(37267.7), f2(9903.8), 33(9625.5), 24(8613.2), cf(8595.4) ...
RK_{4_0}	3f	3f(1321.7), 5e(535.2), 39(328.4), 83(302.9), 04(276.8) ...
RK_{4_1}	df	df(2066.6), e6(510.7), 69(463.6), ad(441.4), 5a(399.3) ...
RK_{4_2}	d7	d7(1367.1), 09(331.8), b5(322.7), be(319.7), 39(313.6) ...
RK_{4_3}	5f	5f(1530.7), cb(409.6), ae(392.4), 1e(373.3), ee(365.7) ...
RK_{5_0}	66	66(5056.0), 4e(938.3), 01(924.7), b6(886.9), 05(870.5) ...
RK_{5_1}	97	97(3577.9), e4(795.5), 54(794.1), 42(674.6), 4a(633.2) ...
RK_{5_2}	2d	2d(6248.1), 5f(1313.0), 5d(1274.5), b3(1180.1), 38(1134.4) ...
RK_{5_3}	4e	4e(6405.4), cc(1363.7), 8d(1173.4), ff(1147.6), 1a(1140.9) ...

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WHAT IS THIS ABOUT?

Broken toys are not charged to our clients

Jack: How will you pay?

Dino: I'll send \$15 by postal order

car = \$3

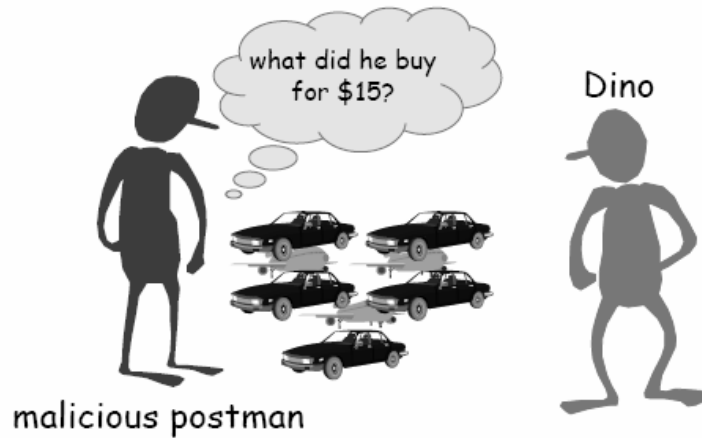
plane = \$5

Dino buys toys from Jack

14/12/2009 Taken from "The Sorcerer's Apprentice Guide to Fault Attacks", FDTC 2006 32

A cartoon illustration of a transaction. On the left, a character named Jack stands next to a display of toys. The display includes six cars and four planes. A sign next to the cars says "car = \$3" and a sign next to the planes says "plane = \$5". A speech bubble from Jack asks, "How will you pay?". On the right, a character named Dino stands with his hands on his hips. A speech bubble from Dino says, "I'll send \$15 by postal order". The scene is enclosed in a rectangular frame with a decorative border. At the top right and bottom left of the frame are small icons of a book and a pencil, respectively.

*The postman wants to know
what Dino bought for \$15*

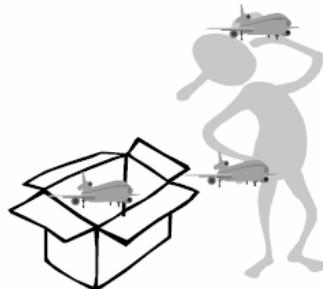


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Taken from "The Sorcerer's Apprentice
Guide to Fault Attacks", FDTC 2006

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*In the meanwhile Jack prepares
the DHL*

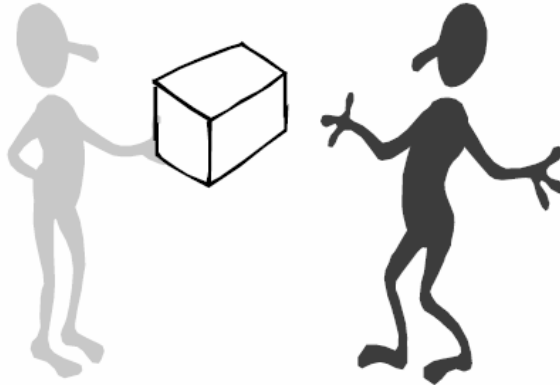


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Taken from "The Sorcerer's Apprentice
Guide to Fault Attacks", FDTC 2006

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and gives it to the postman



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Taken from "The Sorcerer's Apprentice
Guide to Fault Attacks", FDTC 2006

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*Who kicks it strong enough to
break one toy*

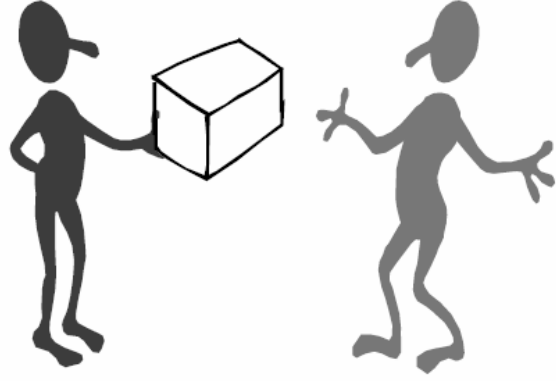


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Taken from "The Sorcerer's Apprentice
Guide to Fault Attacks", FDTC 2006



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and gives it to Dino



14/12/2009 Taken from "The Sorcerer's Apprentice Guide to Fault Attacks", FDTC 2006 37

a week later he monitors Dino's postal order...

Lesson learned: Fault attacks can also extract secrets from tokens!

Hardware faults can have various sources: voltage glitches, light beams, laser beams...

14/12/2009 Taken from "The Sorcerer's Apprentice Guide to Fault Attacks", FDTC 2006 38

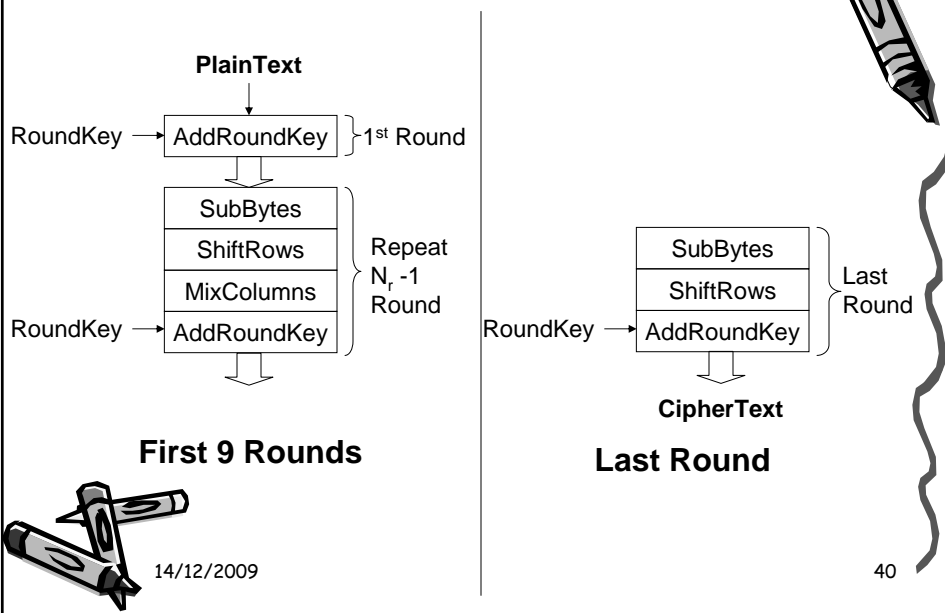
Fault Attacks on Block Ciphers

- Attacks based on induction of faults
 - Both accidental and intentional
- First conceived in 1996 by Boneh, Demillo and Lipton
- E. Biham developed Differential Fault Analysis (DFA) attacker DES
- Optical fault induction attacks : Ross Anderson, Cambridge University - CHES 2002

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AES Algorithm



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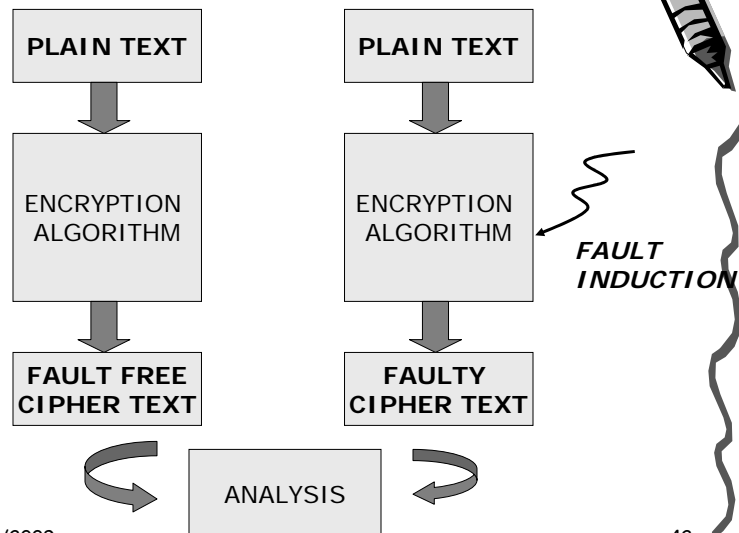
Research on Fault Attacks on AES

- Christophe Giraud, 2003: Byte level faults to the input of 9th round, 250 faulty CTs.
- Blomer et al, 2003: 128 to 256 faulty CTs required
- P. Dusart et al : DFA on AES, bitwise fault between 8th and 9th rounds, 40 faulty CTs required
- Piret et al, Between 8th and 9th rounds, 2 faulty CTs required
- Recently fault attacks have been developed by NTT labs, Japan which exploits the key-scheduling algorithm

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Illustration of a Fault Attack



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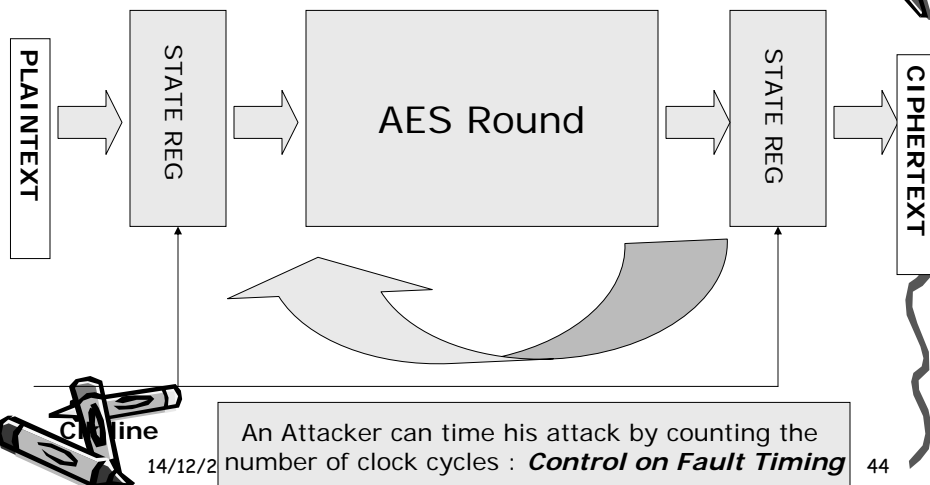
Fault Model Used

- **Single Byte Fault**
 - Attacker induces fault at the input of the 8th round in a single byte
 - Fault value should be non-zero but can be arbitrary
- Relaxing the requirements make the attack more **practical**
 - No knowledge required of the fault value
 - Lesser bytes needed to be faulty
 - Lesser faulty cipher texts required

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A Practical Scenario: An Iterated AES Architecture



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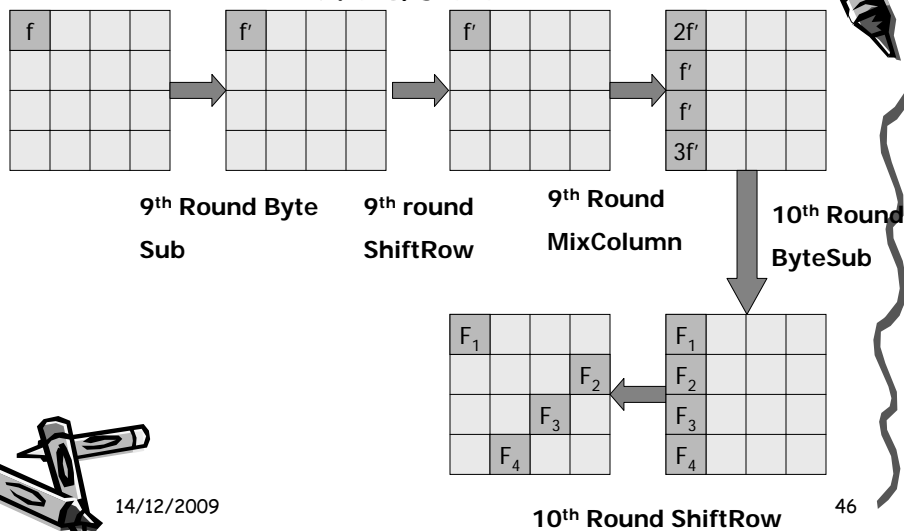
Principle of the Attack

- First, consider a single byte arbitrary fault at the input of the 9th round.
- ISB : Inverse Sub Byte
- We develop a filter, which takes as input the faulty and fault free ciphertext.

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Propagation of Fault Induced



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The patterns gives the following equations:

- $ISB(x_1+K_1)+ISB(x_1+F_1+K_1)=$
 $2[ISB(x_2+K_2)+ISB(x_2+F_2+K_2)]$
- $ISB(x_2+K_2)+ISB(x_2+F_2+K_2)=$
 $ISB(x_3+K_3)+ISB(x_3+F_3+K_3)$
- $ISB(x_4+K_4)+ISB(x_4+F_4+K_4)=$
 $3[ISB(x_2+K_2)+ISB(x_2+F_2+K_2)]$



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Important Points

- No dependency on the fault value.
- Finds out the key using two faulty encryptions with a probability of around 0.99
- Rest of the cases a third faulty cipher text is needed
- Time Complexity is 2^{16} .
- One byte fault reveals 4 key bytes.
 - To obtain the entire key, 4 faulty cipher texts needed.



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When the fault is induced at the 8th Round...

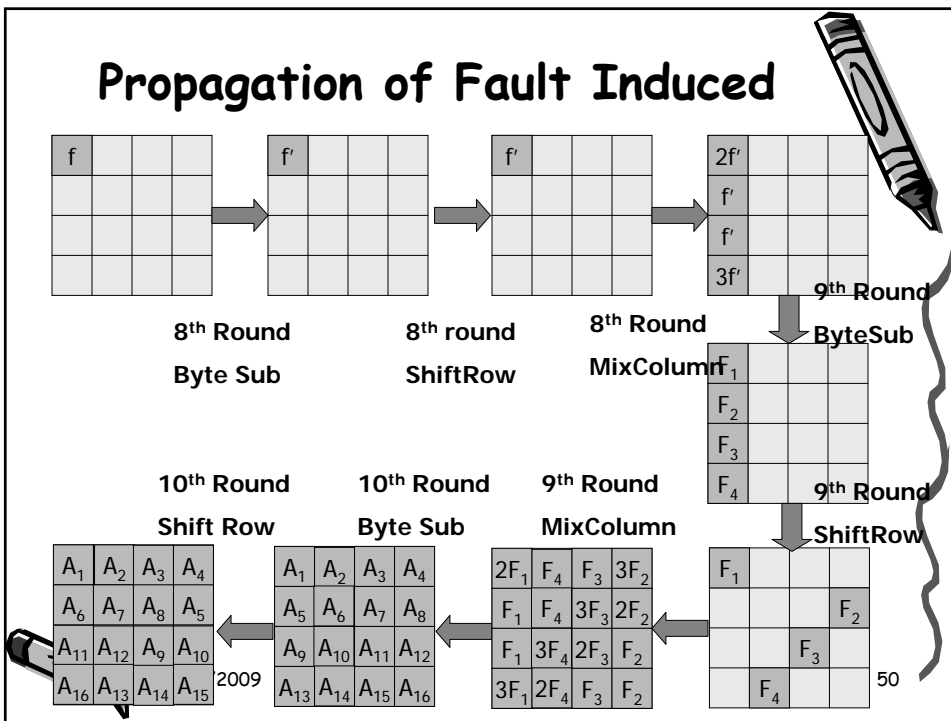
- Fault is induced at the input of 8th round
- A one byte disturbance creates a 4 byte fault at the input of the 9th round
- Let us trace the disturbance through the last 3 rounds
- Equations of similar nature...



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Propagation of Fault Induced



The patterns gives the following equations:

- $ISB(x_1+K_{00})+ISB(x_1+A_1+K_{00})=2[ISB(x_8+K_{13})+ISB(x_8+F_2+K_2)]$
- $ISB(x_8+K_{13})+ISB(x_8+A_5+K_{00})=ISB(x_{11}+K_{22})+ISB(x_{11}+A_9+K_{22})$
- $ISB(x_{14}+K_{31})+ISB(x_{14}+A_{13}+K_{31})=3[ISB(x_8+K_{13})+ISB(x_8+A_5+K_{13})]$



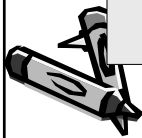
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For the other key bytes...

Similar equations are derived for the other key bytes

For all the equations the worst case complexity is around 2^8 to 2^9 .
Two faulty cipher text pairs reveal the exact key with a high probability.



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Can the attack work with one faulty cipher text?

- With one faulty cipher text:
 - Number of possible values per 4 bytes of the key is around 2^8 .
 - There are 2^{32} possible candidates for 128 bits of the AES key.
 - Brute force key is thus possible!



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Why 2^{32} ?

$$2 \delta_1 = S^{-1}(x_1 \oplus k_1) \oplus S^{-1}(x'_1 \oplus k_1)$$

$$\delta_1 = S^{-1}(x_8 \oplus k_8) \oplus S^{-1}(x'_8 \oplus k_8)$$

$$\delta_1 = S^{-1}(x_{11} \oplus k_{11}) \oplus S^{-1}(x'_{11} \oplus k_{11})$$

$$3 \delta_1 = S^{-1}(x_{14} \oplus k_{14}) \oplus S^{-1}(x'_{14} \oplus k_{14})$$

- There are 4 solutions to the equation:
 $S^{-1}(x) \oplus S^{-1}(x \oplus a) = \beta$
- Thus there are 4 values of k_1, k_8, k_{11}, k_{14} each which satisfies the equations.
 - thus the size of the list is 2^{16}
 - but there are repetitions in the list
 - the maximum size of the list for which there is no repetition with a high probability follows from Birthday Paradox. It is thus 2^8
 - thus the total size of AES key is 2^{32}

Comparison of Existing Fault Attacks

Reference	Fault Model	Fault Loc.	#Faulty CT
Blomer	Force 1 bit to 0	Chosen	128
Giraud	Switch 1 bit	Any bit of chosen bytes	50
Giraud	Disturb 1 byte	Anywhere among 4 bytes	250
Dusart	Disturb 1 byte	Anywhere between last 2 MixColumn	40
Piret	Disturb 1 byte	Anywhere between 7 th & 8 th round MixColumn	2
This Paper	Disturb 1 byte	Anywhere between 7 th round MixColumn and last round input	2

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Comparison with existing fault attacks exploiting key scheduling

Reference	No. of fault injection points	No. of faulty encryptions	Brute force search
Takahashi (NTT Lab)	1	2	2^{18}
	2	4	2^{16}
	3	7	0
Takahashi (NTT Lab)	1	2	2^{40}
	3	7	0
Our Attack	1	2	0
	1	1	2^{32}

Conclusions

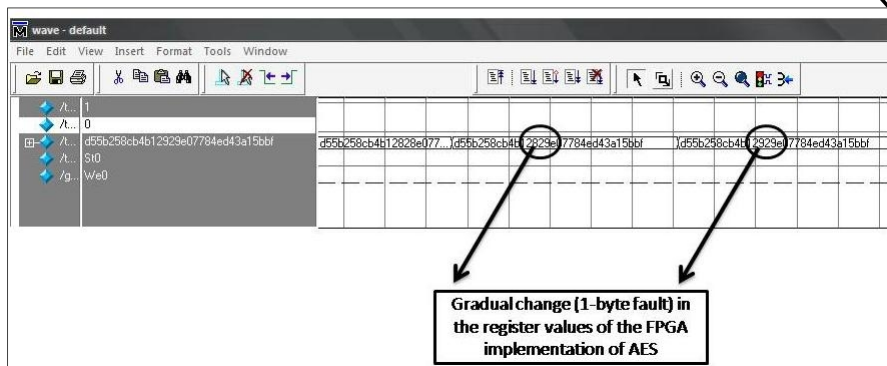
- The work proposes the strongest attack of this class (till date) in literature
 - Using a single byte fault at the input of the 8th round, AES can be broken with a brute force search of:
 - 2^{32} if the byte location is known and
 - 2^{36} if the byte location is unknown.
- Currently, we are working to mount the attack on real life FPGA implementations of AES using less sophisticated techniques, like clock glitching.



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Effect of clock glitches...



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Future Scopes of Research

- Improving the fault attack.
- Performing the fault attack on FPGA by clock glitching.
- Developing counter-measures against Fault attacks:
 - Error correcting Codes may be useful.
- Evaluating whether the fault attack counter-measures reveal other side channel information



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More information in:

Debdeep Mukhopadhyay, *An Improved Fault Based Attack of the Advanced Encryption Standard*. AFRICACRYPT 2009: LNCS 5580, pp 421-434



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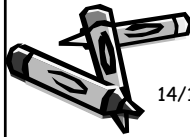
Models of SCA

- Mutual Information (MI) Analysis:
 - Let X and Y be random variables in the discrete space \mathcal{X} and \mathcal{Y} , with probability distributions P_X and P_Y .
 - Then the Mutual Information between X and Y is:

$$I(X:Y) = H(X) - H(X|Y)$$

If X and Y are independent: $I(X:Y) = 0$

If X and Y are dependent: $I(X:Y) = H(X)$

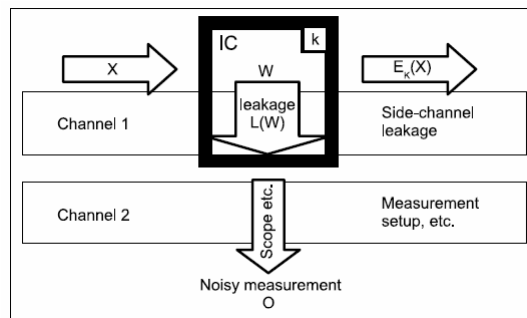


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SCAs using MI

- Channels for Information Leakage:



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Formalizing the Leakage

- Attacker obtains $q > 0$ measurement traces, $o_{x_i}(t)$, for q input values, x_i being processed by $E_k(t)$.
- Leakage Function:
 - $W \rightarrow L$: Information on words is leaked in L
 - $L \rightarrow O$: Information on L is observed through the observable



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The MI based formalism

- Adversary guesses a key value, k and computes $f_k(x)$, which is an intermediate result like $S\text{-Box}(x \oplus k)$
- Usually, W is some bits of $f_k(x)$, say 3 MSB.
 - thus for each guesses key k , there are 8 bins:
 $L_k = \{L_0, \dots, L_7\}_k$
 - Divide the observables for each inputs among this bins.



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Probability Distribution

- Compute the distributions:

$$P_{O|L_i} = \frac{|\{o_{x_j} = o \mid x_j \in L_i\}|}{|L_i|}$$
$$P_o = \frac{|\{o_{x_j} = o\}|}{q}$$



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Entropy and Mutual Information

- These distributions lead to the following entropies:

$$H(O|L_i) \text{ and } H(O)$$

- Define Mutual Information,

$$I(L_k; O) = H(O) - H(O|L_i)$$

- Note that these computations are under assumption of a portion of the key.



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The MI based Distinguisher

- We repeat this process for all possible key values.
- **The attack strategy is to return the key K^* , which maximizes the value of $I(L_k;O)$**
- Also find out the time intervals when the intermediate results are computed.



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An Example

- Target: AES-128
- 8 bit microcontroller (AT90S8515)
- Observable: Voltage drop across a 50 ohm resistor in the ground line
- Sampled at $t=1, \dots, 1800$
- Plaintexts are varied at random
 - the experiment focuses on the first key byte.
 - $q=1000$ power traces are gathered, $O_{x_i}(t)$, $i=1$ to q .



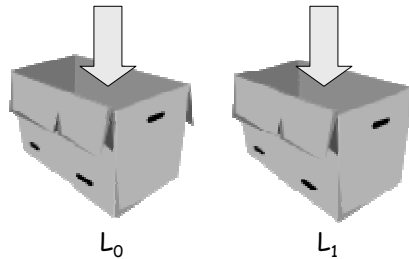
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An Example (contd.)

- Compute $f_k(x) = \text{Sbox}(x \oplus k)$
- The leakage function, L is say the r^{th} bit, if $r=0$ it is the LSB.

Distribute
the x values
into the two
bins



$$P_{o|L_i} = \frac{|\{o_{x_j} = o \mid x_j \in L_i\}|}{|L_i|}$$

$$P_o = \frac{|\{o_{x_j} = o\}|}{q}$$

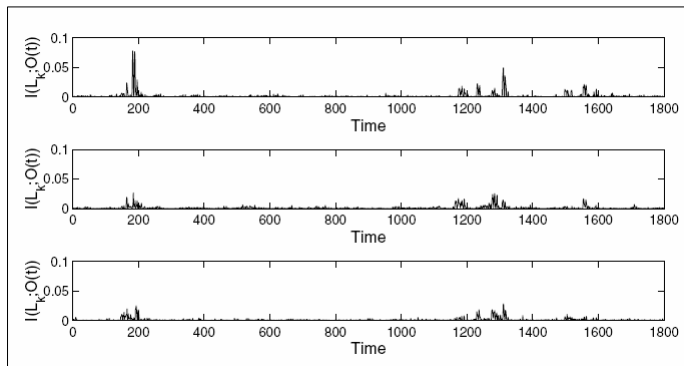
Compute, $I(L_k; O)$



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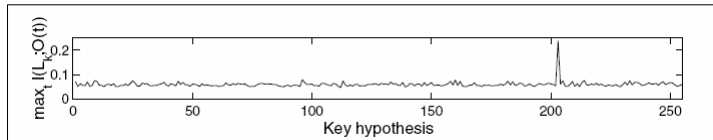
Mutual Information Plots for $r=0, 1$ and 2



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Obtaining the actual key



- The MI values are plotted wrt. the key
- The correct key is expected to exhibit larger MI value.



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Formal Approach to Leakage Resilient Cryptography

“Stefan Dziembowski and Krzysztof Pietrzak”, *Leakage-Resilient Cryptography*, FOCs, 2009

- attempts to solve the problem from of designing cryptosystems whose mathematical descriptions alone certifies their security against all possible forms of side channel attacks!



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The Stream Cipher Ingredients

- Three variables A , B (secret) and K_0 .
- Three loop variables at the input of the j^{th} round:
 M_0^{j-1} , M_1^{j-1} , O_{j-1}
- Initialization: $M_0^0=A$, $M_1^0=B$, $O_0=K_0$



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The Adversary Model

- **“Only Computations leak information”**: Micali and Reyzin, “Physically Observable Cryptography, TCC, pp. 278-296, 2004.
- S (Stream Cipher) only accesses $\tau_j = M_{j \bmod 2}^{j-1}$ in the j^{th} round and the output O_{j-1} .



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The Adversary Model

- The adversary Q can adaptively choose a function f_j with range $\{0,1\}^\lambda$, where $\lambda \ll |\text{secret key}|$
- The adversary gets K_j and $\Lambda_j = f_j(\tau_j)$
- The adversary views $\text{View}_j = [K_0, \dots, K_j, \Lambda_1, \dots, \Lambda_j]$



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Measure of Security

- $S(A, B, K_0) \rightarrow Q$: random experiment where an adversary Q attacks S initialized with a key A, B, K_0
- $\text{view}(S(A, B, K_0) \rightarrow Q)$: view of Q at the end of the attack

$$P_{\text{rand}} = \Pr_{A, B, K_0} [D_{\text{ind}}(\text{view}(S(A, B, K_0) \rightarrow Q, U_k) = 1)]$$

$$P_{\text{real}} = \Pr_{A, B, K_0} [D_{\text{ind}}(\text{view}(S(A, B, K_0) \rightarrow Q, K_j) = 1)]$$

$$\text{AdvInd}(D_{\text{ind}}, Q, S, j) = |P_{\text{real}} - P_{\text{random}}| < \epsilon$$

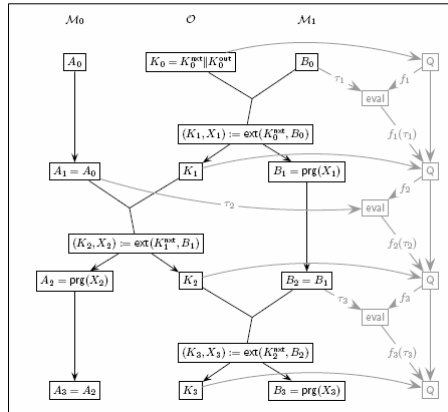


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A leakage resilient stream cipher

- Uses a Pseudorandom generator (PRSG)
- The amount of leakage is at least logarithmic of the internal state



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Conclusion

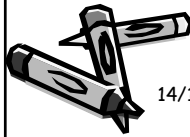
- Side Channel Analysis is an extremely important topic
- We have studied some of the well known side channels in cryptography.
- Need for the development of formalisms:
 - Information Theory
- Side Channel Analysis is now not a topic for only engineers: theoreticians are also worried!



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Moral of the Story



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