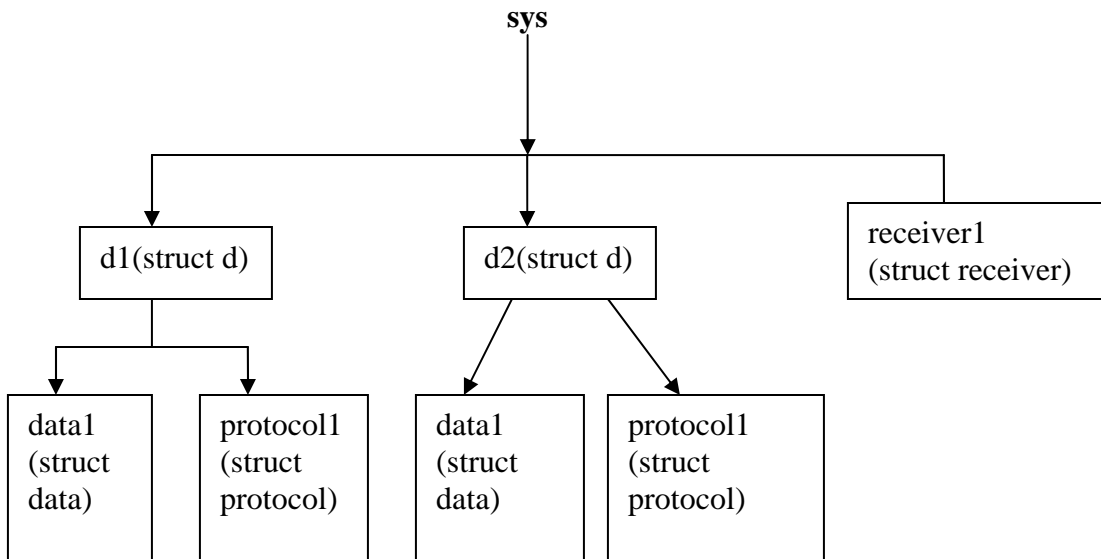


# CS676 : DIGITAL DESIGN VERIFICATION

## Tutorial-1

Date: 18.1.07 Time : 15 mnts

1. Write the basic differences between Formal and Simulation based verification.
2. What is directed and random testing?
3. What is meant by the term equivalence checking and property checking?
4. Write an 'e' construct for the following hierarchy: skip the details inside the struct.



Use the naming convention discussed in the class. This is important.