###### **Bibhas Ghoshal**

### Profile

* Presently persuing P.hD in Computer Science and Engineering from IIT,Kharagpur
* 3 years of experience in handling academic projects related to embedded system design.
* Experience with implementation of software applications running on FPGA .
* User level proficiency in CAD tools for VLSI design(tools from Synopsys and Cadence)
* 5 years of experience in teaching at graduate and post graduate level.

### Skill Set

**Software Tools:** Digital chip design (front-end): ModelSim and Design Compiler

 Digital chip design (back-end): Encounter and Virtuoso

 Circuit simulation: Hspice

 FPGA Implementation: ISE(8-10)

 **Languages:**  C,Verilog,VHDL,Python,Matlab

**O/S:** Linux(RPM and Debian based distributions.),Windows

### Education:

* M.E.in Computer Science and Engineering – 2005,West Bengal University of Technology,(WBUT), West Bengal, India
* M.Sc. in Electronic Science-2002,Jadavpur University,West Bengal, India

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| **Degree/Examination** | **Board/University** | **Year of passing** | **Class/div** |
| **Master of Engineering** | **West Bengal University of Technology** | **2005** | **7.97(CGPA)** |
| **Master of Science** | **Jadavpur University** | **2002** | **1st (79%)** |
| **Bachelor of Science** | **Burdwan University** | **2000** | **1st (67%)** |

### Professional Experience

July 2006-December-2009, Project Officer WBUT,Kolkata India

As a project officer, to work on academic project, by helping the graduate and post-graduate students in their job.

* Guided a group of B.Tech(IT)students in their project work titled “hand written character recognition system”. **Environment: Matlab**
* Guided a group of M.tech(Microelectronics) students in their project on “Low Power Systems”.

Environment: Hspice