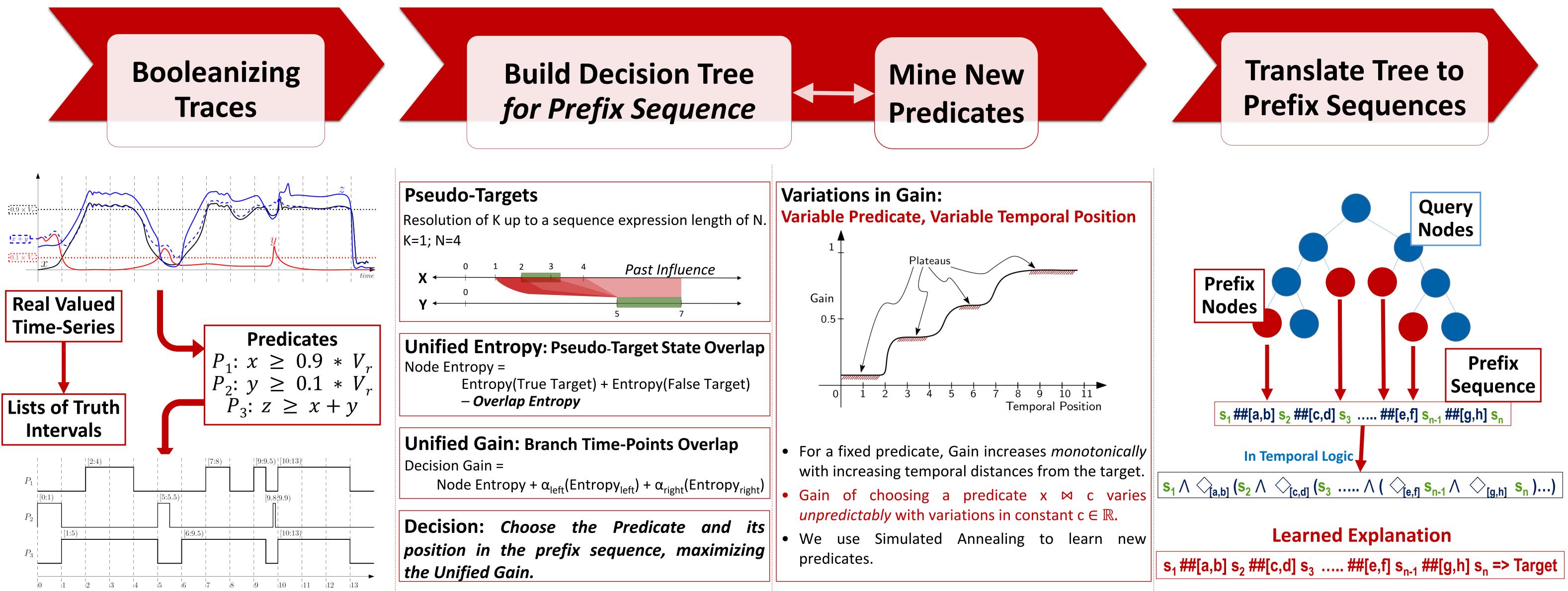


Flexible Mining of Causal Relations from Hybrid System Traces António Bruto da Costa¹, Goran Frehse², Pallab Dasgupta¹ ¹Indian Institute of Technology Kharagpur; ²ENSTA ParisTech France

Acknowledging Partial Support from Intel Labs, Intel Corporation



INTRODUCTION

- Mining Temporal Properties from Time-series data.
 - Filter real properties from incidental ones
- Property Mining: Complex for Continuous / Hybrid Systems.
 - Degrees of influence on a

Interval Arithmetic & Decision Trees can be used to Learn Temporal **Sequences of Events that Explain Observations in Time-Series data.**

WHY?

Root Cause Analysis

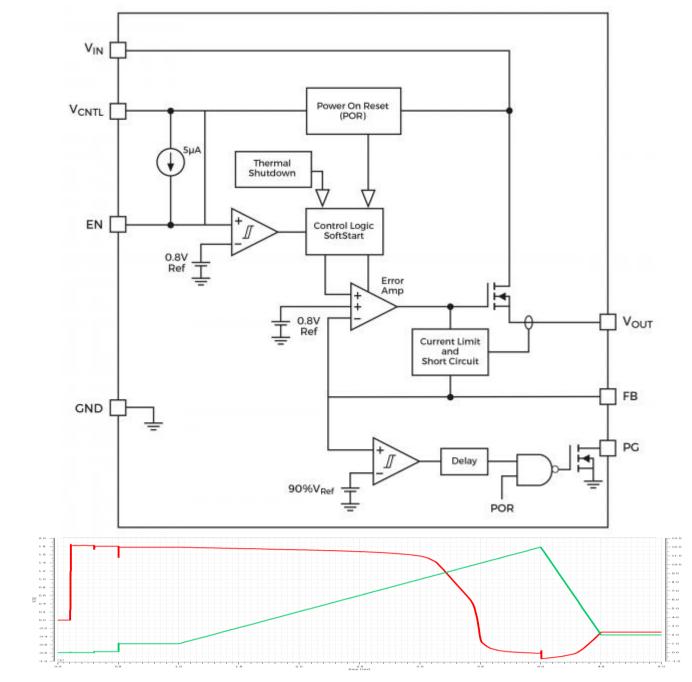
- Infeasible: Manually examine sizeable timeseries & identify common causes.
- **Anomaly Detection**
 - Learned patterns: Detect rare and

consequent - varies continuously - dense time. Parameterized method to learn likely causes of an well defined event.

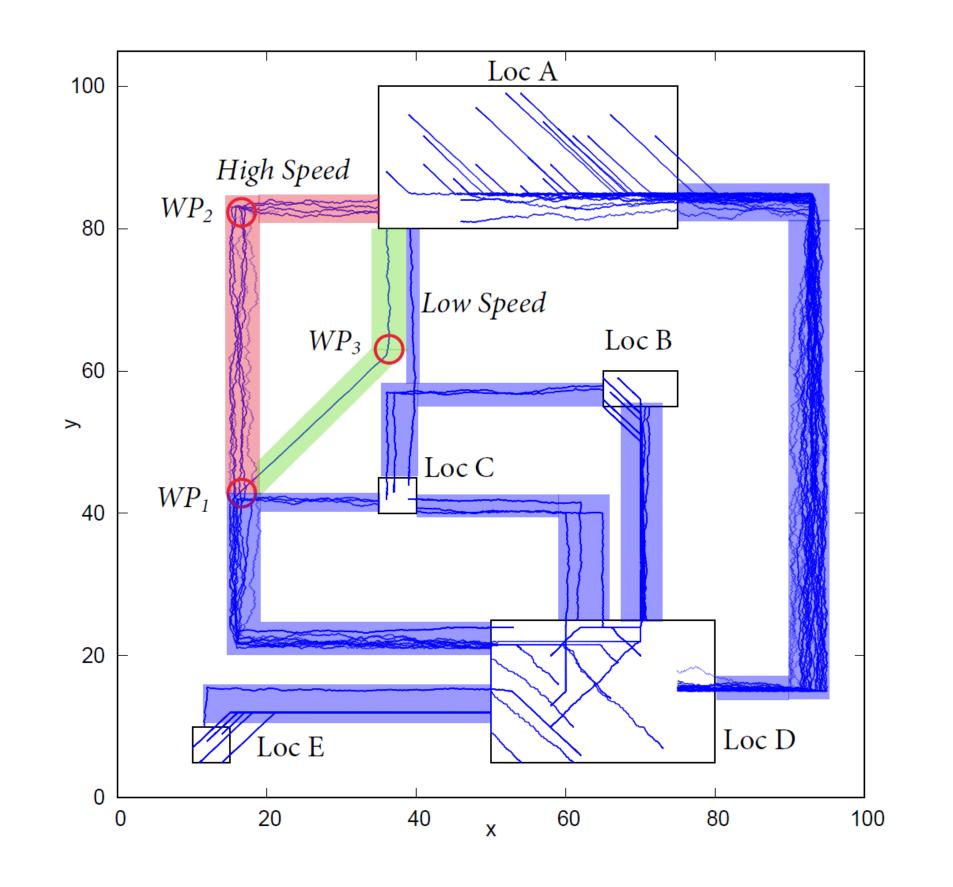
- Interval arithmetic
- Flexible learning Over Time Scales and Predicates over system variables.



Mixed Signal Circuit Properties



Traffic (Vehicular/Network/Other)



suspicious deviations. *****Explaining Classifications

• Derive succinct, human - interpretable explanations for events and their absence.

Applications And Opportunities

Cyber Physical Systems

• Transactional relationships: Patterns of interaction between control and plant. Root cause analysis and anomaly detection

Circuit Design and Testing

- Aid for formal specification writing.
- Design guidance, Root Cause, Timing Analysis.
- Circuit Behavior abstracted and used in an

Low Dropout Regulator Functional block diagram of the AP7176B 3A ultra-low dropout LDO, Diodes Inc.

Circuit Properties Learned (5x10⁵ time points)

The regulator has a **rise time** of 4us !InShrtCkt && VLowerBand |=> ##[4.06us : 4.09us] VUpperBand

The regulator enters a **short circuit state** when the voltage is under 1.6V and the current is above 8.5V. !VAbove 66 ShrtCktEvent |=>##[0:20us] InShrtCkt Traffic Patterns (2.5x10⁵ time points)

Movement from Loc A to Loc D with **timing** LocA |=> ##[1h24m50s : 2h3m] LocD

From LocD, passing through waypoint-WP2 within 1h to 6h, it is possible to reach LocA within the following 1h. LocD ##[1h4m : 6h11m22s] WP_2 => ##[0h : 1h] LocA

Assume/Guarantee reasoning framework. Process Control

- Root cause analysis, Process optimization. Business and Finance
 - Learning from transactional patterns.

Music and the Arts

• Understanding how artists compose, the structure of musical movements, frequently used patterns.

*****Behavioral Sciences

• Pattern extraction, understanding and explaining behaviours.

*****Medicine

Root cause analysis.