

$$L \subseteq NL$$

$L \stackrel{?}{=} NL \rightarrow$ NL-Completeness
defined in terms of log-space reductions.

Logspace Reductions

$$f: \{0,1\}^* \rightarrow \{0,1\}^*$$

polynomially bounded f_n .
[for any $x \in \{0,1\}^*$, $|f(x)| \leq |x|^c$
for some c .]

f is implicitly log-space computable if
there exists a $\log(|x|)$ -space DTM that
given input (x, i) , outputs $f(x)|_i$ provided
 $i \leq |f(x)|$.

$f(x)|_i$
↓
 i^{th} bit of $f(x)$.

$$A, B \subseteq \{0,1\}^*$$

A is log-space reducible to B , denoted $A \leq_l B$,
if \exists an implicitly log-space computable fn.
 $f: \{0,1\}^* \rightarrow \{0,1\}^*$ s.t. $x \in A$ iff $f(x) \in B$
 $\forall x \in \{0,1\}^*$

Properties of \leq_l

1. \leq_l transitive

2. $A \leq_l B$ & $B \in L$, then $A \in L$

1. $A \leq_e B \leq_e C$
 f g

M, N : log-space machines
 computing f, g

$A \leq_e C$
 $h = g \circ f$

K : log-space machine
 computing $g \circ f$.

K : on i/p (x, j) must o/p $g(f(x))|_j$

Simulates N whenever

N needs to access i^{th} bit of $f(x)$, it pauses N , storing N 's worktape contents. Then simulates M on x to obtain $f(x)|_i$ & then resumes N .

K should
 compute $g(f(x))|_j$
 without storing
 $f(x)$.

$\rightarrow O(\log|x|)$ -space since N is a log-space machine.

Storing indices i / j : $O(\log |f(x)|)$ / $O(\log |g(x)|)$
 $\equiv O(\log |x|)$ since f, g are polynomially bounded.

Storing N 's worktape contents : $O(\log |x|)$

Total space requirements for K : $O(\log |x|)$.

NL-Completeness

A language A is NL-Complete if it is in
 NL & $\forall B \in NL, B \leq_e A$.

PATH = $\{(G, s, t) : \exists \text{ a path in } G \text{ from } s \text{ to } t\}$

Thm : PATH is NL-Complete

Proof :

I PATH \in NL

[Maintain a counter k of no. of vertices visited] $k = 0$
Write down u on the worktape. (Initially $u = s$)
Non-deterministically choose a neighbour v of u
 $u \leftarrow v$; $k++$
If $u = t$ & $k \leq n (= |V(G)|)$, then accept.
 \rightarrow uses $O(\log n)$ space to store u, v & k

II PATH is NL-Complete

$A \in \text{NL}$, $A \leq_e \text{PATH}$.

\downarrow
 $\exists \log\text{-space NDTM } M \text{ deciding } A.$

Reduction: $x \longmapsto (G_{M,x}, C_{\text{start}}, C_{\text{accept}})$

\uparrow
configuration graph of M on ip x .

\swarrow
 $\log\text{-space computable.}$

$C_{\text{start}}, C_{\text{accept}}$: can be represented using $O(\log |x|)$ bits.

Consider the adjacency matrix of $G_{M,x}$
Every bit of $G_{M,x}$ can be obtained in $\log(|x|)$ -space.

Bit at row c , column c' : $\begin{cases} 1 & \text{if } (c, c') \in E(G_{M, n}) \\ 0 & \text{o.w} \end{cases}$

✓
can be determined by writing down
& simulating M 's action in config c &
checking if the next config. is c' .

c, c'
↓
 $O(\log |n|)$
space.